

BANK 60

1E00h	Core Registers	1E24h	PWMIN0PPS	1E47h	SSP1CLKPPS
1E0Bh		1E25h	PWMIN1PPS	1E48h	SSP1DATPPS
1E0Ch		1E26h	PWM1ERSPPS	1E49h	SSP1SSPPS
1E0Dh		1E27h	PWM2ERSPPS	1E4Ah	SSP2CLKPPS
1E0Eh		1E28h	Unimplemented Read as '0'	1E4Bh	SSP2DATPPS
1E0Fh	T1CKIPPS	1E38h		1E4Ch	SSP2SSPPS
1E10h	T1GPPS	1E39h	CWG1PPS	1E4Dh	Unimplemented Read as '0'
1E11h	T3CKIPPS	1E3Ah	Unimplemented Read as '0'	1E4Fh	
1E12h	T3GPPS	1E3Ch		1E50h	ADACTPPS
1E13h	Unimplemented Read as '0'	1E3Dh	CLCIN0PPS	1E51h	Unimplemented Read as '0'
1E18h		1E3Eh	CLCIN1PPS	1E56h	
1E19h	T2INPPS	1E3Fh	CLCIN2PPS	1E57h	APMCLKPPS
1E1Ah	T4INPPS	1E40h	CLCIN3PPS	1E58h	PORTWIN0PPS
1E1Bh	—	1E41h	CK1PPS	1E59h	PORTWIN1PPS
1E1Ch	—	1E42h	RX1PPS	1E5Ah	PORTWCLKPPS
1E1Dh	—	1E43h	CK2PPS	1E5Bh	Unimplemented Read as '0'
1E1Eh	CCP1PPS	1E44h	RX2PPS	1E6Fh	
1E1Fh	CCP2PPS	1E45h	Unimplemented Read as '0'	1E70h	Common RAM (Accesses 70h-7Fh)
1E20h	Unimplemented Read as '0'	1E46h		1E7Fh	
1E23h					

Legend:



Unimplemented data memory locations, read as '0'