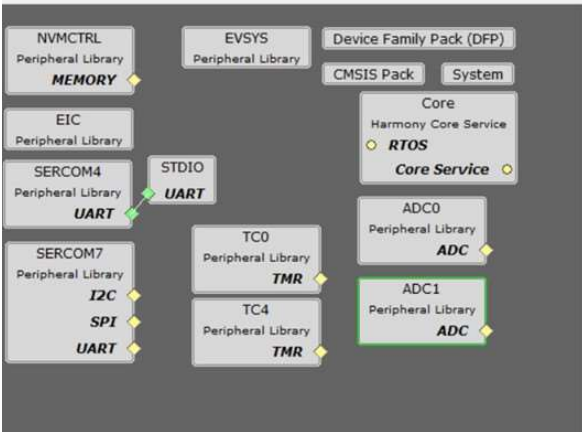




ADC1

Enable Slave ☐

Select Prescaler

Peripheral clock divided by 256 ▾

Select Sample Length (cycles)

16 ▴ ▾

**** Conversion Time is 119.466666667 uS ****

Select Reference

AVDD ▾

Select Conversion Trigger

Free Run ▾

Enable DMA Sequencing ☐

Channel Configuration

Select Positive Input

ADC AIN8 Pin ▾

Select Negative Input

Internal Ground ▾

Result Configuration

Select Result Resolution

12-bit result ▾

Left Aligned Result ☐

Enable Result Ready Interrupt ☒

Enable Result Ready Event Out ☐

Window Mode Configuration

Sleep Mode Configuration