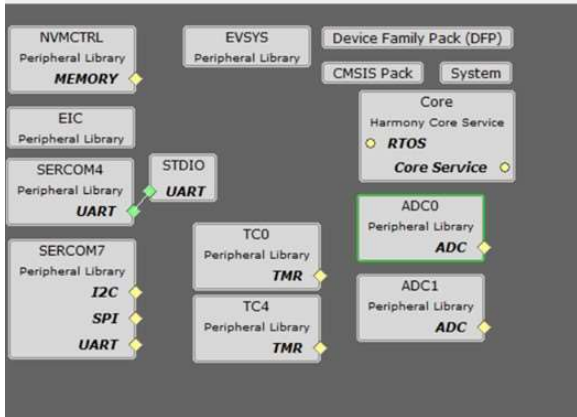




ADC0

Select Prescaler Peripheral clock divided by 32

Select Sample Length (cycles) 32

**** Conversion Time is 23.4666666667 uS ****

Select Reference AVDD

Select Conversion Trigger Free Run

Enable DMA Sequencing ☐

Channel Configuration

Select Positive Input ADC AIN2 Pin

Select Negative Input Internal Ground

Result Configuration

Window Mode Configuration

Select Window Monitor Mode $I(WINLT < RESULT < WINUT)$

Window Upper Threshold 2,400

Window Lower Threshold 500

Enable Window Monitor Interrupt ☒

Enable Window Monitor Event Out ☐

Sleep Mode Configuration