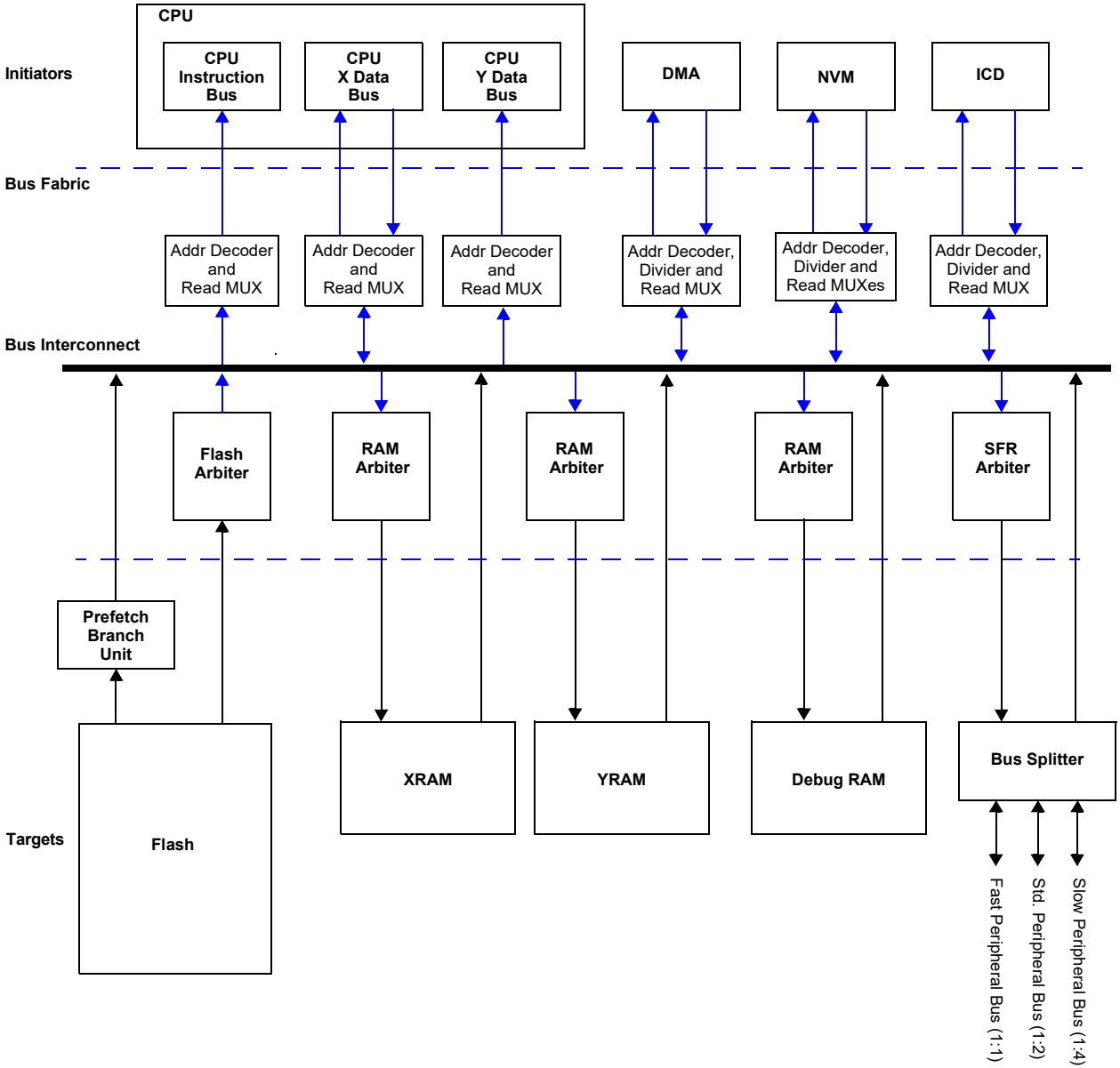
CPU
CPU Instruction Bus
CPU X Data Bus
CPU Y Data Bus
DMA
NVM
ICD
Initiators
Bus Fabric
Addr Decoder and Read MUX
Addr Decoder and Read MUX
Addr Decoder and Read MUX
Addr Decoder, Divider and Read MUX
Addr Decoder, Divider and Read MUXes
Addr Decoder, Divider and Read MUX
Bus Interconnect
Flash Arbiter
RAM Arbiter
RAM Arbiter
RAM Arbiter
SFR Arbiter
Prefetch Branch Unit
Flash
XRAM
YRAM
Debug RAM
Bus Splitter
Targets
Fast Peripheral Bus (1:1)
Std. Peripheral Bus (1:2)
Slow Peripheral Bus (1:4)