


BANK 61

1E80h	Core Registers
1E8Bh	
1E8Ch	ANSELA
1E8Dh	WPUA
1E8Eh	ODCONA
1E8Fh	SLRCONA
1E90h	INLVLA
1E91h	IOCAP
1E92h	IOCAN
1E93h	IOCAF
1E94h	—
1E95h	—
1E96h	ANSELB ⁽²⁾
1E97h	WPUB ⁽²⁾
1E98h	ODCONB ⁽²⁾
1E99h	SLRCONB ⁽²⁾
1E9Ah	INLVLB ⁽²⁾
1E9Bh	IOCBP ⁽²⁾
1E9Ch	IOCBN ⁽²⁾
1E9Dh	IOCBF ⁽²⁾
1E9Eh	—
1E9Fh	—
1EA0h	ANSELC ⁽⁴⁾
1EA1h	WPUC ⁽⁴⁾
1EA2h	ODCONC ⁽⁴⁾
1EA3h	SLRCONC ⁽⁴⁾
1EA4h	INLVLC ⁽⁴⁾
1EA5h	IOCCP ⁽⁴⁾
1EA6h	IOCCN ⁽⁴⁾
1EA7h	IOCCF ⁽⁴⁾
1EA8h	Unimplemented Read as '0'
1EE0h	
1EE1h	RA1I2C ⁽¹⁾
1EE2h	RA2I2C ⁽¹⁾
1EE3h	—
1EE4h	—
1EE5h	RB4I2C ⁽²⁾
1EE6h	—
1EE7h	RB6I2C ⁽²⁾
1EE8h	—
1EE9h	RC0I2C ⁽³⁾
1EEAh	RC1I2C ⁽³⁾
1EEBh	Unimplemented Read as '0'
1EEFh	
1EF0h	Common RAM (Accesses 70h-7Fh)
1EFFh	

Note:

- 1. Available on 8-pin devices only**
- 2. Available on 20-pin devices only**
- 3. Available on 14-pin devices only**
- 4. Available on 14-20-pin devices only**

Legend:

 Unimplemented data memory locations, read as '0'