

| BANK 0 | | BANK 1 | | BANK 2 | | BANK 3 | | BANK 4 | | BANK 5 | | BANK 6 | | BANK 7 | |
|--------|---------------------------------------|--------|---------------------------------------|--------|---------------------------------------|--------|---------------------------------------|--------|---------------------------------------|--------|---------------------------------------|--------|--|--------|--|
| 000h | Core Registers | 080h | Core Registers | 100h | Core Registers | 180h | Core Registers | 200h | Core Registers | 280h | Core Registers | 300h | Core Registers | 380h | Core Registers |
| 00Bh | | 08Bh | | 10Bh | | 18Bh | | 20Bh | | 28Bh | | 30Bh | | 38Bh | |
| 00Ch | PORTA | 08Ch | PIR0 | 10Ch | PMD0 | 18Ch | WDTCON0 | 20Ch | FVRCON | 28Ch | CPUDOZE | 30Ch | TMR1L | 38Ch | T2TMR |
| 00Dh | PORTB ⁽¹⁾ | 08Dh | PIR1 | 10Dh | PMD1 | 18Dh | WDTCON1 | 20Dh | CPCON | 28Dh | OSCCON1 | 30Dh | TMR1H | 38Dh | T2PR |
| 00Eh | PORTC ⁽²⁾ | 08Eh | PIR2 | 10Eh | PMD2 | 18Eh | WDTPSL | 20Eh | — | 28Eh | OSCCON2 | 30Eh | T1CON | 38Eh | T2CON |
| 00Fh | — | 08Fh | PIR3 | 10Fh | PMD3 | 18Fh | WDTPSH | 20Fh | — | 28Fh | OSCCON3 | 30Fh | T1GCON | 38Fh | T2HLT |
| 010h | — | 090h | PIR4 | 110h | PMD4 | 190h | WDTTMR | 210h | HLVDCON0 | 290h | OSCCON3 | 310h | T1GATE | 390h | T2CLKCON |
| 011h | — | 091h | PIR5 | 111h | — | 191h | BORCON | 211h | HLVDCON1 | 291h | OSCCON3 | 311h | T1CLK | 391h | T2RST |
| 012h | TRISA | 092h | PIR6 | 112h | — | 192h | PCON0 | 212h | — | 292h | OSCCON3 | 312h | TMR3L | 392h | T4TMR |
| 013h | TRISB ⁽¹⁾ | 093h | PIR7 | 113h | — | 193h | PCON1 | 213h | — | 293h | OSCCON3 | 313h | TMR3H | 393h | T4PR |
| 014h | TRISC ⁽²⁾ | 094h | — | 114h | — | 194h | — | 214h | — | 294h | OSCCON3 | 314h | T3CON | 394h | T4CON |
| 015h | — | 095h | — | 115h | — | 195h | — | 215h | — | 295h | — | 315h | T3GCON | 395h | T4HLT |
| 016h | — | 096h | PIE0 | 116h | — | 196h | — | 216h | — | 296h | CLKRCON | 316h | T3GATE | 396h | T4CLKCON |
| 017h | — | 097h | PIE1 | 117h | — | 197h | — | 217h | — | 297h | CLKRCLK | 317h | T3CLK | 397h | T4RST |
| 018h | LATA | 098h | PIE2 | 118h | — | 198h | — | 218h | — | 298h | — | 318h | — | 398h | — |
| 019h | LATB ⁽¹⁾ | 099h | PIE3 | 119h | — | 199h | — | 219h | — | 299h | — | 319h | — | 399h | — |
| 01Ah | LATC ⁽²⁾ | 09Ah | PIE4 | 11Ah | — | 19Ah | — | 21Ah | — | 29Ah | ICSPLOCK | 31Ah | — | 39Ah | — |
| 01Bh | — | 09Bh | PIE5 | 11Bh | — | 19Bh | — | 21Bh | — | 29Bh | ICSPEN | 31Bh | — | 39Bh | — |
| 01Ch | — | 09Ch | PIE6 | 11Ch | — | 19Ch | TMR0L | 21Ch | — | 29Ch | — | 31Ch | — | 39Ch | — |
| 01Dh | — | 09Dh | PIE7 | 11Dh | — | 19Dh | TMR0H | 21Dh | — | 29Dh | — | 31Dh | — | 39Dh | — |
| 01Eh | — | 09Eh | — | 11Eh | — | 19Eh | TOCON0 | 21Eh | — | 29Eh | — | 31Eh | — | 39Eh | — |
| 01Fh | — | 09Fh | — | 11Fh | — | 19Fh | TOCON1 | 21Fh | — | 29Fh | — | 31Fh | — | 39Fh | — |
| 020h | General Purpose Registers 80 Bytes | 0A0h | General Purpose Registers 80 Bytes | 120h | General Purpose Registers 80 Bytes | 1A0h | General Purpose Registers 80 Bytes | 220h | General Purpose Registers 80 Bytes | 2A0h | General Purpose Registers 80 Bytes | 320h | General Purpose Registers 16 Bytes | 3A0h | General Purpose Registers 80 Bytes ⁽³⁾ |
| 06Fh | | 0EFh | | 16Fh | | 1EFh | | 26Fh | | 2EFh | | 32Fh | General Purpose Registers 64 Bytes ⁽³⁾ | 3EFh | |
| 070h | Common RAM (Accesses 70h-7Fh) | 0F0h | Common RAM (Accesses 70h-7Fh) | 170h | Common RAM (Accesses 70h-7Fh) | 1F0h | Common RAM (Accesses 70h-7Fh) | 270h | Common RAM (Accesses 70h-7Fh) | 2F0h | Common RAM (Accesses 70h-7Fh) | 370h | Common RAM (Accesses 70h-7Fh) | 3F0h | Common RAM (Accesses 70h-7Fh) |
| 07Fh | | 0FFh | | 17Fh | | 1FFh | | 27Fh | | 2FFh | | 37Fh | | 3FFh | |

Note:

- 1. Available on 20-pin devices only
- 2. Available on 14/20-pin devices only
- 2. Not available on PIC16F13224/44 devices

Legend:

Unimplemented data memory locations, read as '0'