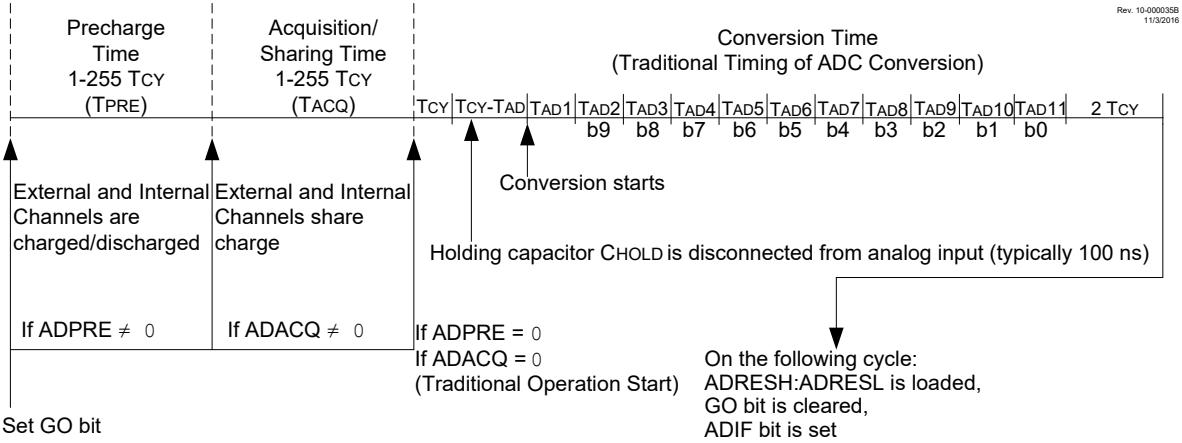
Rev. 10-000035B
11/3/2016
Precharge Time 1-255 TCY (TPRE)
Acquisition/ Sharing Time 1-255 TCY (TACQ)
Conversion Time (Traditional Timing of ADC Conversion)
TCY
TCY-TAD
TAD1
TAD2
TAD3
TAD4
TAD5
TAD6
TAD7
TAD8
TAD9
TAD10
TAD11
2 TCY
b9
b8
b7
b6
b5
b4
b3
b2
b1
b0
Conversion starts
External and Internal Channels are charged/discharged
External and Internal Channels share charge
Holding capacitor CHOLD is disconnected from analog input (typically 100 ns)
If ADPRE ≠ 0
If ADACQ ≠ 0
If ADPRE = 0
If ADACQ = 0
(Traditional Operation Start)
On the following cycle:
ADRESH:ADRESL is loaded,
GO bit is cleared,
ADIF bit is set
Set GO bit