

## BANK 62

|       |                              |       |                              |       |                                     |
|-------|------------------------------|-------|------------------------------|-------|-------------------------------------|
| 1F00h | Core Registers               | 1F2Fh | Unimplemented<br>Read as '0' | 1F5Fh | APMPERE1L                           |
| 1F0Bh |                              | 1F4Bh |                              | 1F60h | APMPERE1H                           |
| 1F0Ch |                              | 1F4Ch |                              | 1F61h | APMPERE1U                           |
| 1F0Dh |                              | 1F4Dh |                              | 1F62h | APMEND2L                            |
| 1F0Eh | UMTOAP                       | 1F4Eh | APMPREL                      | 1F63h | APMEND2H                            |
| 1F0Fh | UMTOAL                       | 1F4Fh | APMPREH                      | 1F64h | APMEND2U                            |
| 1F21h | UMTOAH                       | 1F50h | APMPERL                      | 1F65h | APMPERE2L                           |
| 1F22h | Unimplemented<br>Read as '0' | 1F51h | APMPERH                      | 1F66h | APMPERE2H                           |
| 1F23h |                              | 1F52h | APMSTART1L                   | 1F67h | APMPERE2U                           |
| 1F24h |                              | 1F53h | APMSTART1H                   | 1F68h | APMCLK                              |
| 1F25h |                              | 1F54h | APMPERS1L                    | 1F69h | APMSTATUSL                          |
| 1F26h | PORTW                        | 1F55h | APMPERS1H                    | 1F6Ah | APMSTATUSH                          |
| 1F27h | LATW                         | 1F56h | APMPERS1U                    | 1F6Bh | APMSTATUSU                          |
| 1F28h | PORTWIN0                     | 1F57h | APMSTART2L                   | 1F6Ch | Unimplemented<br>Read as '0'        |
| 1F29h | PORTWIN1                     | 1F58h | APMSTART2H                   |       |                                     |
| 1F2Ah | PORTWIN2                     | 1F59h | APMSTART2U                   |       |                                     |
| 1F2Bh | PORTWIN3                     | 1F5Ah | APMPERS2L                    |       |                                     |
| 1F2Ch | PORTWIN4                     | 1F5Bh | APMPERS2H                    | 1F6Fh | Common RAM<br>(Accesses<br>70h-7Fh) |
| 1F2Dh | PORTWIN5                     | 1F5Ch | APMPERS2U                    | 1F70h |                                     |
| 1F2Eh | PORTWCLK                     | 1F5Dh | APMEND1L                     | 1F71h |                                     |
|       | PORTWDF                      | 1F5Eh | APMEND1H                     |       |                                     |
|       | PORTWCON                     |       | APMEND1U                     |       |                                     |

Legend:



Unimplemented data memory locations, read as '0'