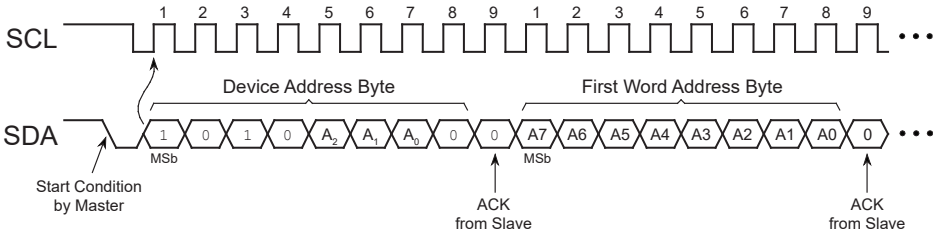
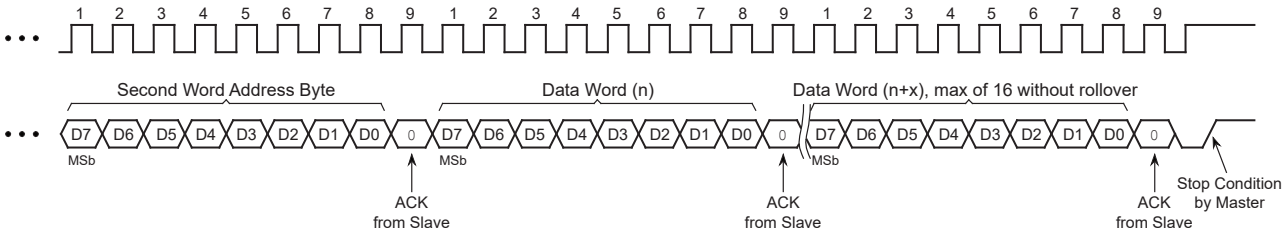

SCL
SDA
1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9
Device Address Byte
First Word Address Byte
1 0 1 0 A2 A1 A0 0 0 A7 A6 A5 A4 A3 A2 A1 A0 0
MSb
MSb
Start Condition by Master
ACK from Slave
ACK from Slave


1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9 1 2 3 4 5 6 7 8 9
Second Word Address Byte
Data Word (n)
Data Word (n+x), max of 16 without rollover
D7 D6 D5 D4 D3 D2 D1 D0 0 D7 D6 D5 D4 D3 D2 D1 D0 0 D7 D6 D5 D4 D3 D2 D1 D0 0
MSb
MSb
MSb
ACK from Slave
ACK from Slave
ACK from Slave
Stop Condition by Master