

BANK 56		BANK 57		BANK 58		BANK 59		BANK 60		BANK 61		BANK 62		BANK 63	
1C00h	Core Registers	1C80h	Core Registers	1D00h	Core Registers	1D80h	Core Registers	1E00h	Core Registers	1E80h	Core Registers	1F00h	Core Registers	1F80h	Core Registers
1C0Bh	Unimplemented Read as '0'	1C8Bh	NVMADRL	1D0Ch	ADLTHL	1D88h	RA0PPS	1E0Bh	See Table 2 for register mapping details	1E8Bh	See Table 3 for register mapping details	1F0Bh	UMTOAP	1F8Bh	Unimplemented Read as '0'
1C0Ch		1C8Ch	NVMADRH	1D0Dh	ADLTHH	1D8Dh	RA1PPS	1E0Ch		1E8Ch		1F0Ch	UMTOAL	1F8Ch	
		1C8Dh	NVMDATL	1D0Eh	ADUTHL	1D8Eh	RA2PPS					1F0Dh	UMTOAH		
		1C8Eh	NVMDATH	1D0Fh	ADUTHH	1D8Fh	—					1F0Eh	Unimplemented Read as '0'		
		1C8Fh	NVMCON1	1D10h	ADERRL	1D90h	RA4PPS					1F0Fh			
		1C90h	NVMCON2	1D11h	ADERRH	1D91h	RA5PPS								
		1C91h	Unimplemented Read as '0'	1D12h	ADSTPTL	1D92h	Unimplemented Read as '0'								
		1C92h		1D13h	ADSTPTH	1D97h	Unimplemented Read as '0'								
				1D14h	ADFLTRL	1D98h									
				1D15h	ADFLTRH	1D99h									
				1D16h	ADACCL	1D9Ah									
				1D17h	ADACCH	1D9Bh									
				1D18h	ADACCU	1D9Ch									
				1D19h	ADCNT	1D9Dh									
				1D1Ah	ADRPT	1D9Eh									
				1D1Bh	ADPREVL	1D9Fh									
				1D1Ch	ADPREVH	1DA0h									
				1D1Dh	ADRESL	1DA1h									
				1D1Eh	ADRESH	1DA2h									
				1D1Fh	ADPCH	1DA3h									
				1D20h	ADNCH	1DA4h									
				1D21h	ADACQL		Unimplemented Read as '0'							1FE3h	STATUS_SHAD
				1D22h	ADACQH									1FE4h	WREG_SHAD
				1D23h	ADCAP									1FE5h	BSR_SHAD
				1D24h	ADPREL									1FE6h	PCLATH_SHAD
				1D25h	ADPREH									1FE7h	FSROL_SHAD
				1D26h	ADCON0									1FE8h	FSROH_SHAD
				1D27h	ADCON1									1FE9h	FSR1L_SHAD
				1D28h	ADCON2									1FEAh	FSR1H_SHAD
				1D29h	ADCON3									1FEBh	—
				1D2Ah	ADSTAT									1FEDh	STKPTR
				1D2Bh	ADREF									1FEEh	TOSL
				1D2Ch	ADACT									1FEFh	TOSH
				1DCDh	ADCLK									1FF0h	Common RAM (Accesses 70h-7Fh)
				1D2Eh	ADCG1A									1FF1h	
				1D2Fh	ADCG1B ⁽¹⁾									1FF2h	
				1D30h	ADCG1C ⁽²⁾									1FF3h	
				1D31h	Unimplemented Read as '0'									1FF4h	
1C6Fh	Common RAM (Accesses 70h-7Fh)	1CEFh	Common RAM (Accesses 70h-7Fh)	1D6Fh	Common RAM (Accesses 70h-7Fh)	1DEFh	Common RAM (Accesses 70h-7Fh)	1E6Fh	Common RAM (Accesses 70h-7Fh)	1EEFh	Common RAM (Accesses 70h-7Fh)	1F6Fh	Common RAM (Accesses 70h-7Fh)	1F6Fh	Common RAM (Accesses 70h-7Fh)
1C70h		1CF0h		1D70h		1DF0h		1E70h		1EF0h		1F70h		1F70h	
1C7Fh		1CFFh		1D7Fh		1DFFh		1E7Fh		1EFFh		1F7Fh		1FFFh	

Note:

- 1. 20-pin devices only
- 2. 14/16/20-pin devices only

Legend:

 Unimplemented data memory locations, read as '0'