BANK 48			BANK 49		BANK 50		BANK 51	BANK 52		BANK 53		BANK 54		BANK 55	
1800h		1880h		1900h		1980h		1A00h		1A80h		1B00h		1B80h	
	Core Registers		Core Registers		Core Registers		Core Registers		Core Registers		Core Registers		Core Registers		Core Registers
180Bh		188Bh		190Bh		198Bh		1A0Bh		1A8Bh		1B0Bh		1B8Bh	
180Ch	_	188Ch	_	190Ch	-	198Ch	_	1A0Ch	_	1A8Ch	_	1B0Ch	_	1B8Ch	_
180Dh	_	188Dh	_	190Dh	_	198Dh	_	1A0Dh	_	1A8Dh	_	1B0Dh	_	1B8Dh	_
180Eh	_	188Eh	-	190Eh	_	198Eh	_	1A0Eh	1	1A8Eh	-	1B0Eh	ı	1B8Eh	_
180Fh	_	188Fh	_	190Fh	_	198Fh	_	1A0Fh	-	1A8Fh	ı	1B0Fh	I	1B8Fh	_
1810h	_	1890h	_	1910h	_	1990h	_	1A10h	-	1A90h	ı	1B10h	ı	1B90h	_
1811h	_	1891h	_	1911h		1991h		1A11h	_	1A91h	_	1B11h	_	1B91h	_
1812h	_	1892h	_	1912h		1992h		1A12h	_	1A92h	_	1B12h	_	1B92h	_
1813h	_	1893h	_	1913h		1993h		1A13h	_	1A93h	_	1B13h	_	1B93h	_
1814h	_	1894h	_	1914h	_	1994h	_	1A14h	_	1A94h	_	1B14h	1	1B94h	_
1815h	_	1895h	_	1915h	_	1995h	_	1A15h	_	1A95h	_	1B15h	1	1B95h	_
1816h	_	1896h	_	1916h	_	1996h	_	1A16h	_	1A96h	_	1B16h	1	1B96h	_
1817h	_	1897h	_	1917h	_	1997h	_	1A17h	_	1A97h	_	1B17h	1	1B97h	_
1818h	_	1898h	_	1918h	_	1998h	_	1A18h	_	1A98h	_	1B18h	-	1B98h	_
1819h	_	1899h	_	1919h	_	1999h	_	1A19h		1A99h	1	1B19h	1	1B99h	_
181Ah	_	189Ah	_	191Ah	_	199Ah	_	1A1Ah	-	1A9Ah	ı	1B1Ah	ı	1B9Ah	_
181Bh	_	189Bh	_	191Bh	_	199Bh	_	1A1Bh	1	1A9Bh	İ	1B1Bh	I	1B9Bh	_
181Ch	_	189Ch	_	191Ch	_	199Ch	_	1A1Ch	-	1A9Ch	ı	1B1Ch	ı	1B9Ch	_
181Dh	_	189Dh	_	191Dh	_	199Dh	_	1A1Dh		1A9Dh	1	1B1Dh	1	1B9Dh	_
181Eh	_	189Eh	_	191Eh	_	199Eh	_	1A1Eh	-	1A9Eh	ı	1B1Eh	ı	1B9Eh	_
181Fh	_	189Fh	_	191Fh	_	199Fh	_	1A1Fh	_	1A9Fh	_	1B1Fh	1	1B9Fh	_
1820h		18A0h		1920h		19A0h		1A20h		1AA0h		1B20h		1BA0h	
	Unimplemented		Unimplemented		Unimplemented		Unimplemented		Unimplemented		Unimplemented		Unimplemented		Unimplemented
	Read as '0'		Read as '0'		Read as '0'		Read as '0'		Read as '0'		Read as '0'		Read as '0'		Read as '0'
186Fh		18EFh		196Fh		19EFh		1A6Fh		1AEFh		1B6Fh		1BEFh	
1870h	Common RAM	18F0h	Common RAM	1970h	Common RAM	19F0h	Common RAM	1A70h	Common RAM	1AF0h	Common RAM	1B70h	Common RAM	1BF0h	Common RAM
	(Accesses		(Accesses		(Accesses		(Accesses		(Accesses		(Accesses		(Accesses		(Accesses
187Fh	70h-7Fh)	18FFh	70h-7Fh)	197Fh	70h-7Fh)	19FFh	70h-7Fh)	1A7Fh	70h-7Fh)	1AFFh	70h-7Fh)	1B7Fh	70h-7Fh)	1BFFh	70h-7Fh)
_		_		_		_									
	Legend:														
	Unimplem	ented da	ta memory locations	, read as	'0'										