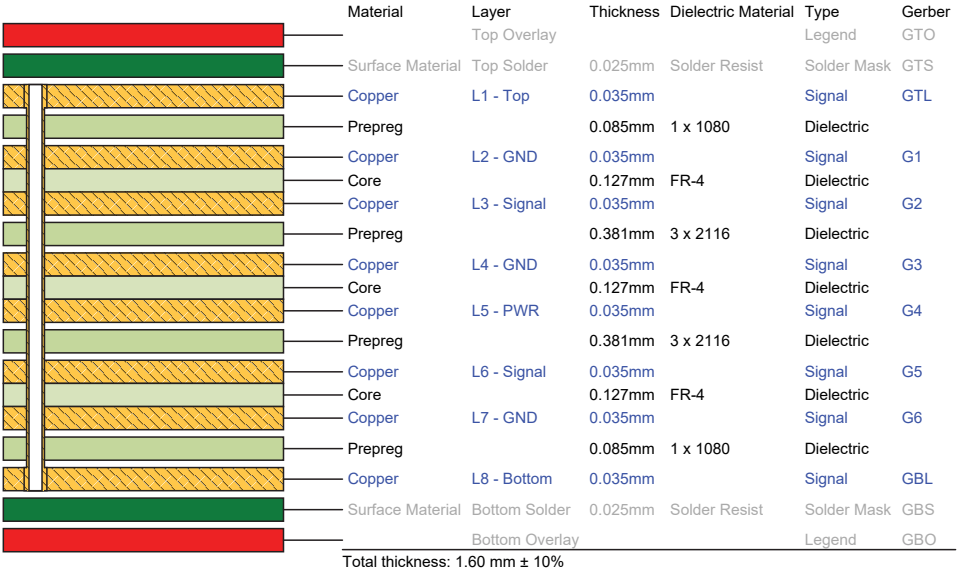


8-Layer Stack Legend



PCB Impedance Information

TYPE	IMPEDANCE	TOLERANCE	LAYER	REFERENCE	WIDTH [um]	GAP [um]
DIFF	90Ω	± 10%	L1	L2	125	200
DIFF	90Ω	± 10%	L8	L7	125	200
DIFF	100Ω	± 10%	L1	L2	100	200
DIFF	100Ω	± 10%	L8	L7	100	200
SE	50Ω	± 10%	L1	L2	125	
SE	50Ω	± 10%	L8	L7	125	
SE	50Ω	± 10%	L3	L2;L4	125	
SE	50Ω	± 10%	L6	L5;L7	125	