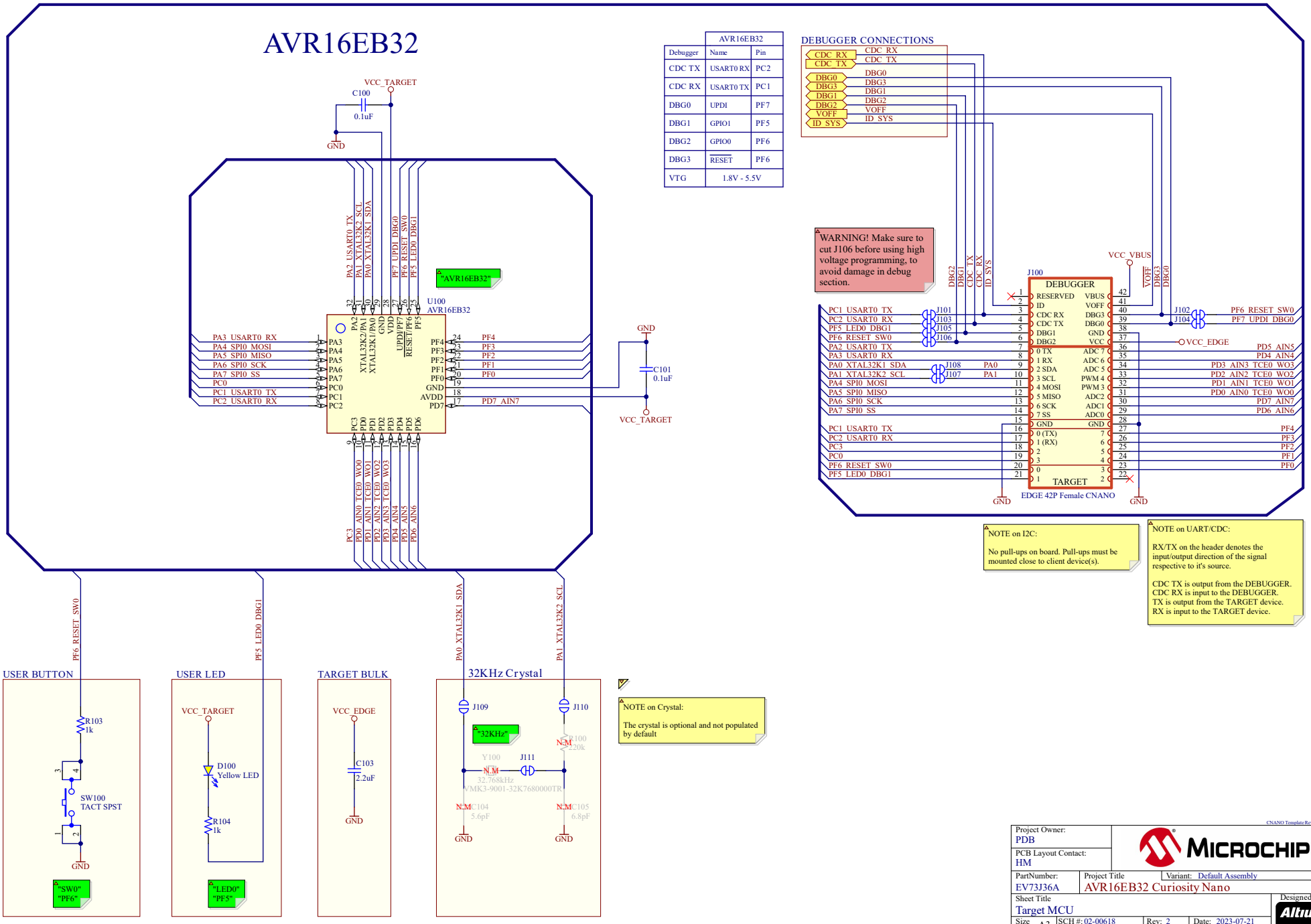
AVR16EB32



	AVR16EB32	
Debugger	Name	Pin
CDC TX	USART0 RX	PC2
CDC RX	USART0 TX	PC1
DBG0	UPDI	PF7
DBG1	GPIO1	PF5
DBG2	GPIO0	PF6
DBG3	$\overline{\text{RESET}}$	PF6
VTG	1.8V - 5.5V	

DEBUGGER CONNECTIONS

NOTE on I2C:

No pull-ups on board. Pull-ups must be mounted close to client device(s).

NOTE on UART/CDC:

RX/TX on the header denotes the input/output direction of the signal respective to it's source.

CDC TX is output from the DEBUGGER.
CDC RX is input to the DEBUGGER.
TX is output from the TARGET device.
RX is input to the TARGET device.

NOTE on Crystal:

The crystal is optional and not populated by default

CNANO Template Revision: 2.3

Project Owner: PDB			
PCB Layout Contact: HM			
PartNumber: EV73J36A	Project Title: AVR16EB32 Curiosity Nano		Variant: Default Assembly
Sheet Title: Target MCU			Designed with Altium
Size: A3	SCH #: 02-00618	Rev: 2	Date: 2023-07-21
	PCB #: 04-11710	Rev: 2	Sheet 2 of 4

File: AVR16EB32_Curiosity_Nano_Target_MCU.SchDoc

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