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**PTP Clock's Configuration and Status**
**Clock Type and Profile**

Clock Instance	HW Domain	Device Type	Profile	Apply Profile Defaults	Filter Type
1	1	Slaveonly	1588	Apply	BASIC

**Port Enable and Configuration**

Port Enable								Configuration
1	2	3	4	5	6	7	8	Ports Configuration
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

**Virtual Port Enable and Configuration**

Enable	Class	Accuracy	Variance	Pri1	Pri2	Local Prio	
False	6	33	65535	128	128	128	
Mode	inp-pin	out-pin	Tod	Pim-port	pps-delay	alarm	
none	0	3	none	1	0	False	
UtcOffset	Valid	leap59	leap61	Time Trac	Freq Trac	ptp Time Scale	Time Source
0	False	False	False	True	True	True	32
Leap Pending		Leap Date		Leap Type			
False		1970-01-01		leap61			

**Local Clock Current Time**

PTP Time	Clock Adjustment method
1970-01-01T00:00:00.000000 033,464,271	Internal Timer <span style="float: right;">Synchronize to System Clock</span>

**Clock Current Data Set**

stpRm	Offset From Master	Mean Path Delay
0	0.000,000,000,000	0.000,000,000,000

**Clock Parent Data Set**

Parent Port ID	port	PStat	Var	Rate	GrandMaster ID	GrandMaster Clock Quality	Pri1	Pri2
00:05:5a:ff:fe:a0:1a:14	0	False	0	0	00:05:5a:ff:fe:a0:1a:14	Cl:255 Ac:Unknwn Va:65535	255	128

**Clock Default Data Set**

Device Type	One-Way	2 Step Flag	Ports	Clock Identity	Dom	Clock Quality
Slaveonly	False	True	8	00:05:5a:ff:fe:a0:1a:14	0	Cl:255 Ac:Unknwn Va:65535
Pri1	Pri2	Local Prio	Protocol		PCP	DSCP
255	128	128	Ethernet		0	0

**Clock Time Properties Data Set**

UtcOffset	Valid	leap59	leap61	Time Trac	Freq Trac	ptp Time Scale	Time Source
0	False	False	False	False	False	True	160
Leap Pending		Leap Date		Leap Type			
False		1970-01-01		leap61			

**Basic Filter Parameters**

Delay Filter	Period	Dist
0	10	0