

IES008 X +

Not secure 192.168.0.58/index.htm

# MICROCHIP PD-IES008-DC240W

- ERPS
- Media Redundancy
- ▶ DHCPv4
- ▶ DHCPv6
- ▶ Security
- ▶ Aggregation
- ▶ Link OAM
- Loop Protection
- ▶ Spanning Tree
- ▶ IPMC Profile
- MVR
- ▶ IPMC
- ▶ LLDP
- PoE
- SyncE
- MAC Table
- ▶ VLANs
- ▶ VLAN Translation
- ▶ Private VLANs
- ▶ VCL
- ▶ Voice VLAN
- ▼ QoS
  - Port Classification
  - Port Policies

## FRER Configuration

### Configuration

Instance	Mode	Enable	FRER VLAN	Recovery					Latent Error Detection					
				Algorithm	History Length	Reset Timeout	Take-no-sequence	Individual	Terminate	Enable	Error Diff	Period	Paths	Reset Period
1	Recovery	<input type="checkbox"/>	1	Vector	2	1000	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	100	2000	2	30000

### Streams

Ingress Streams List
30

### Ports

Egress Port List
1

Save Reset Cancel