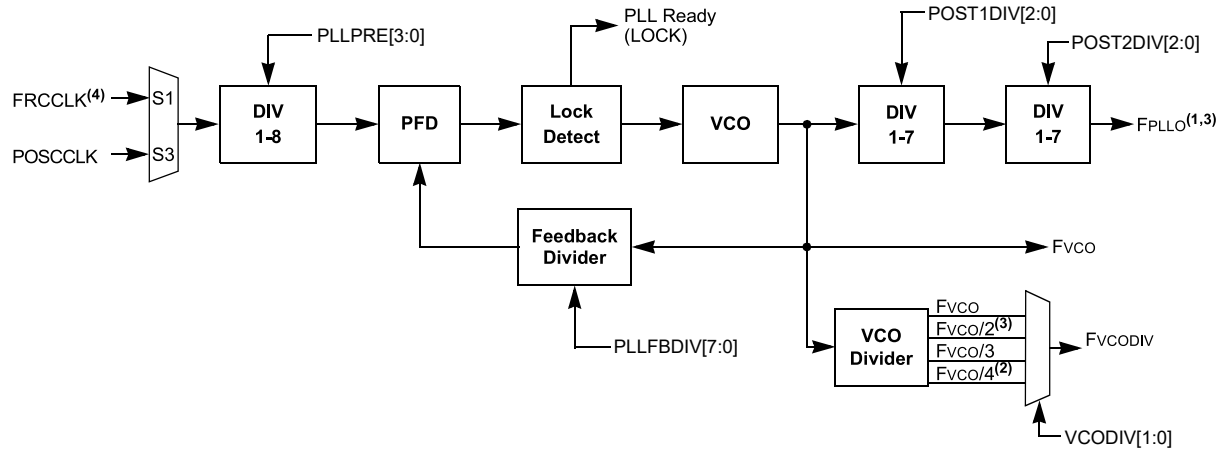




- Note 1:** Clock option for PWM.
Note 2: Clock option for ADC.
Note 3: Clock option for DAC.
Note 4: PLL source is always FRC unless FNOSC is the Primary Oscillator with PLL.