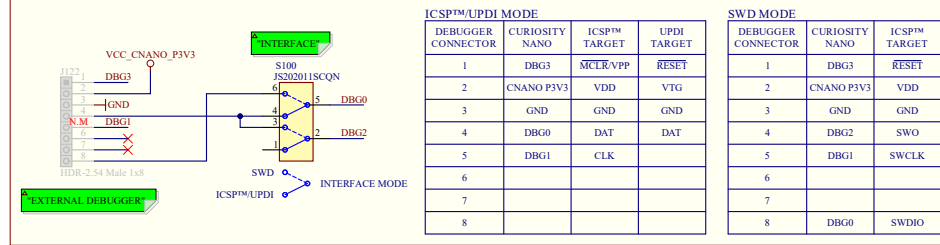


Curiosity Nano

External debugging/programming interface:

- ICSP/UPDI mode for connecting to PIC and AVR based CNAOs.
- SWD mode for debugging/programming ARM based CNAOs.

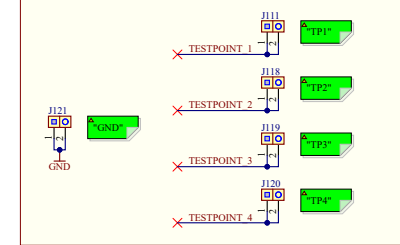
EXTERNAL DEBUGGER CONNECTOR



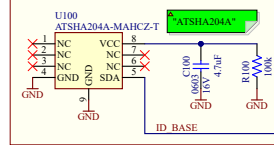
Custom Test Points

- Located at board edge for accessibility with e.g. scope probes.
- Connect any signal to test point using a jumper wire.

CUSTOM TEST POINTS



KIT ID



In combination with connected CNANO provides kit identification and documentation in MPLAB X IDE's 'Kit Window'.

CNANO Socket

The Explorer is designed for fixed 3.3V operation.

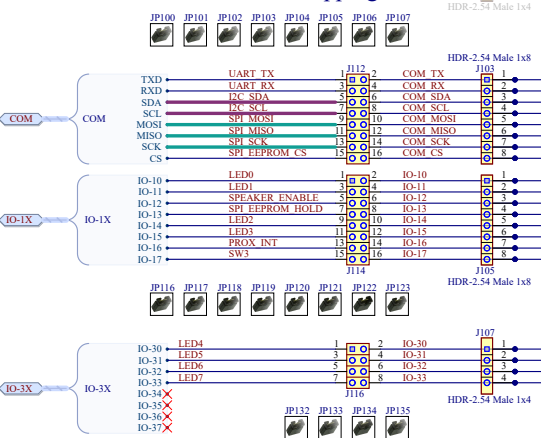
- VOFF on CNANO is forced low through cut-strap J102, to disable its on-board power supply.
- The Explorer will supply a fixed voltage of 3.3V (VCC_CNANO_P3V3) to the CNANO through its VTG pin.

CNANO RESET BUTTON

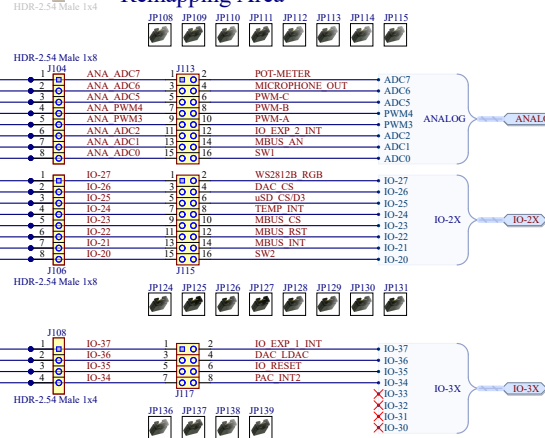


RESET functionality may be disabled on your microcontroller, consult device datasheet for details.

Remapping Area



Remapping Area



IO-XY naming convention groups pins by section (X[1-3]) and pin number (Y[0-7]).

Remapping Area:

- Pin jumper connected to dual row pin headers, connecting Explorer peripherals to the CNANO Socket.
- Disconnect jumpers and use jumper wires to change mapping between Explorer peripherals and the CNANO Socket.
- Single row pin headers can be used for both signal measurements and remapping.

Project Owner:

AH

PCB Layout Contact:

TF, VL

PartNumber:

EV58G97A

Project Title

Curiosity Nano Explorer

Variant: Default Assembly

Sheet Title

Curiosity Nano

Size A3

SCHE: 0240630

Rev: 4

Date: 2025-01-31

File: Curiosity Nano Explorer Curiosity Nano SchDoc

CNANO Template Revision: 2.6



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