

BANK 60

1E00h	Core Registers	1E3Ah	Unimplemented Read as '0'
1E0Bh		1E3Ch	
1E0Ch		1E3Dh	CLCIN0PPS
1E0Dh		1E3Eh	CLCIN1PPS
1E0Eh		1E3Fh	CLCIN2PPS
1E0Fh	T0CKIPPS	1E40h	CLCIN3PPS
1E10h	T1GPPS	1E41h	DT1PPS
1E11h	T3CKIPPS	1E42h	RX1PPS
1E12h	T3GPPS	1E43h	DT2PPS
1E13h	Unimplemented Read as '0'	1E44h	RX2PPS
1E18h		1E45h	—
1E19h		1E46h	—
1E1Ah	T2INPPS	1E47h	SSP1CLKPPS
1E1Bh	T4INPPS	1E48h	SSP1DATPPS
1E1Ch	T6INPPS	1E49h	SSP1SSPPS
1E1Dh	—	1E4Ah	SSP2CLKPPS
1E1Eh	—	1E4Bh	SSP2DATPPS
1E1Fh	CCP1PPS	1E4Ch	SSP2SSPPS
1E20h	CCP2PPS	1E4Dh	Unimplemented Read as '0'
1E23h	Unimplemented Read as '0'	1E4Fh	
1E24h		1E50h	
1E25h		1E51h	ADACTPPS
1E26h	PWMIN0PPS	1E52h	—
1E27h	PWMIN1PPS	1E53h	—
1E28h	PWM1ERSPPS	1E54h	OPA1PPS
1E29h	PWM2ERSPPS	Unimplemented Read as '0'	Unimplemented Read as '0'
1E2Ah	PWM3ERSPPS		
1E2Bh	PWM4ERSPPS		
1E2Ch	Unimplemented Read as '0'	1E6Fh	Common RAM (Accesses 70h-7Fh)
1E2Dh		1E70h	
1E38h	CWG1PPS	1E7Fh	

Legend: Unimplemented data memory locations, read as '0'