NAND Flash PROGRAM PAGE Operation

Software	Hardware Accelerator
Configure PMECC : error correction capability sector size/page size NAND write field set to true spare area desired layout	
Move the NAND Page to external Memory whether using DMA or Processor	**PMECC computes** redundancy as the data is written into external memory
Copy redundancy from PMECC user interface to user defined spare area. using DMA or Processor.	

NAND Flash READ PAGE Operation

Software	Hardware Accelerator
Configure PMECC : error correction capability sector size/page size NAND write field set to false spare area desired layout	
Move the NAND Page from external Memory whether using DMA or Processor	**PMECC computes** polynomial remainders as the data is read from external memory
	PMECC modules indicate if at least one error is detected
If a sector is corrupted use the substitute() function to determine the syndromes	
When the table of syndromes is completed, use the get_sigma() function to get the error location polynomial	
Find the error positions finding the roots of the error location polynomial. And correct the bits	**This step can** be hardware assisted using the PMERRLOC module