

SPI Host:
Microcontroller

Data Clock (SCK)

Data Output (SO)

Data Input (SI)

$\overline{\text{CS}}3$ $\overline{\text{CS}}2$ $\overline{\text{CS}}1$ $\overline{\text{CS}}0$

SI SO SCK

Client 0
AT25XXX

$\overline{\text{CS}}$

SI SO SCK

Client 1
AT25XXX

$\overline{\text{CS}}$

SI SO SCK

Client 2
AT25XXX

$\overline{\text{CS}}$

SI SO SCK

Client 3
AT25XXX

$\overline{\text{CS}}$

