

START

Write HyperFlash™
"Write to Buffer" commands

QSPI Configuration

Write the address in QSPI_IAR

Configure QSPI_IFR
according to the targeted memory
(HFWBEN set to '1')

Scrambling
required?

Configure QSPI_SKR
and QSPI_SMR

Wait until QSPI_SR.SYNCBSY=0,
write QSPI_CR.UPDCFG,
wait until QSPI_SR.SYNCBSY=0

Wait until flag QSPI_ISR.TDRE=1
by polling or interrupt

Data Write

Write QSPI_TDR

Last data
to write?

Wait for flag QSPI_ISR.TXEMPTY
by polling or interrupt

End of Frame

Wait until QSPI_SR.SYNCBSY=0
then
write QSPI_CR.LASTXFER

Wait for flag QSPI_ISR.CSRA
to rise by polling or interrupt

Write HyperFlash™
"Program Buffer to Flash
(confirm)" command

END