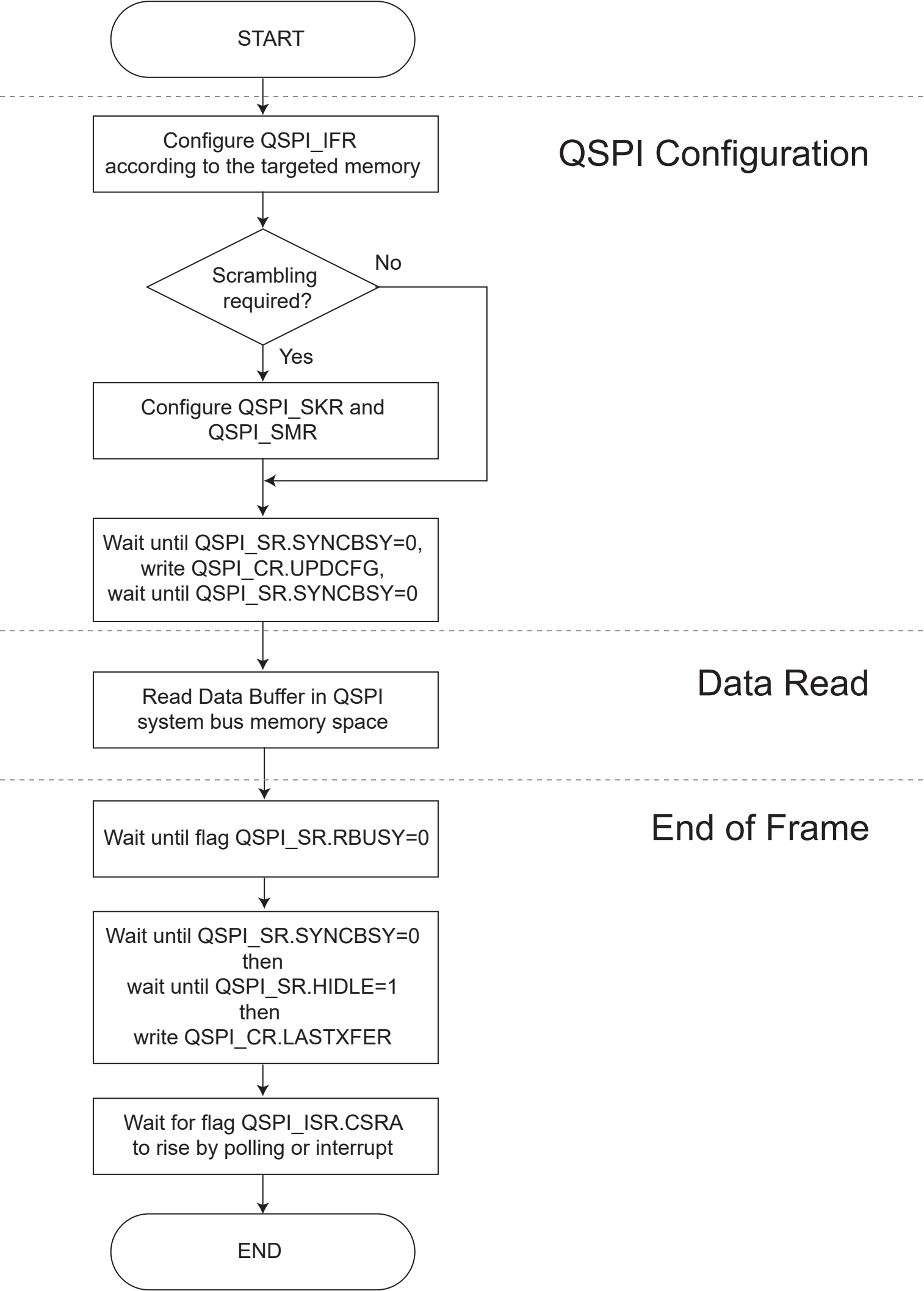
START
QSPI Configuration
Configure QSPI_IFR according to the targeted memory
Scrambling required?
No
Yes
Configure QSPI_SKR and QSPI_SMR
Wait until QSPI_SR.SYNCBSY=0, write QSPI_CR.UPDCFG, wait until QSPI_SR.SYNCBSY=0
Data Read
Read Data Buffer in QSPI system bus memory space
End of Frame
Wait until flag QSPI_SR.RBUSY=0
Wait until QSPI_SR.SYNCBSY=0 then wait until QSPI_SR.HIDLE=1 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt
END