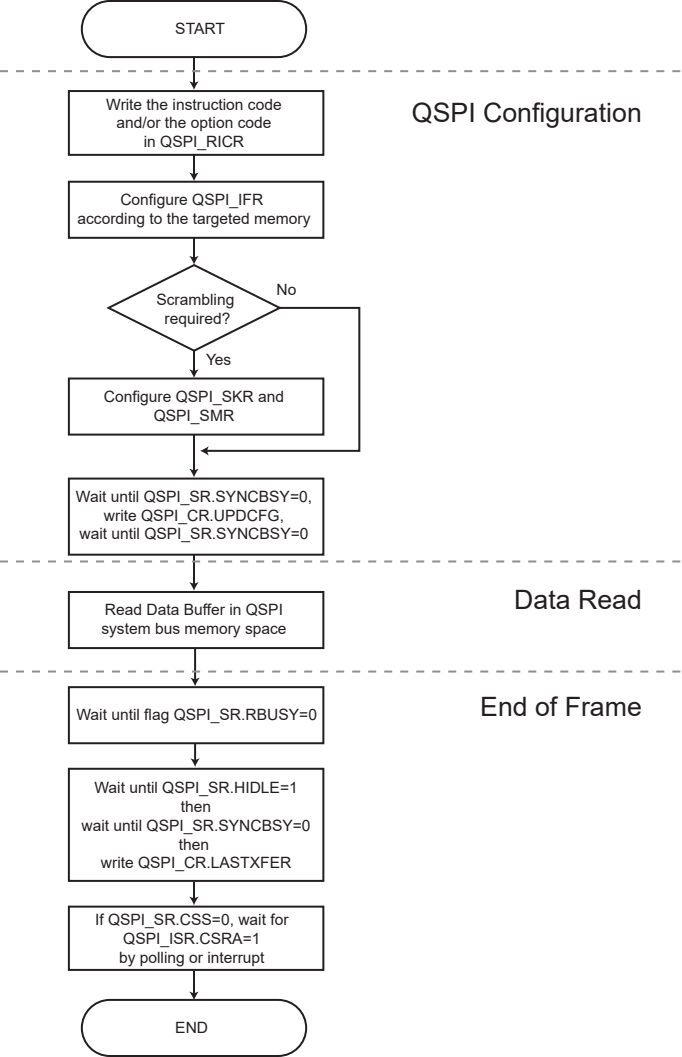
START
QSPI Configuration
Write the instruction code and/or the option code in QSPI_RICR
Configure QSPI_IFR according to the targeted memory
Scrambling required?
No
Yes
Configure QSPI_SKR and QSPI_SMR
Wait until QSPI_SR.SYNCBSY=0, write QSPI_CR.UPDCFG, wait until QSPI_SR.SYNCBSY=0
Data Read
Read Data Buffer in QSPI system bus memory space
End of Frame
Wait until flag QSPI_SR.RBUSY=0
Wait until QSPI_SR.HIDLE=1 then wait until QSPI_SR.SYNCBSY=0 then write QSPI_CR.LASTXFER
If QSPI_SR.CSS=0, wait for QSPI_ISR.CSRA=1 by polling or interrupt
END