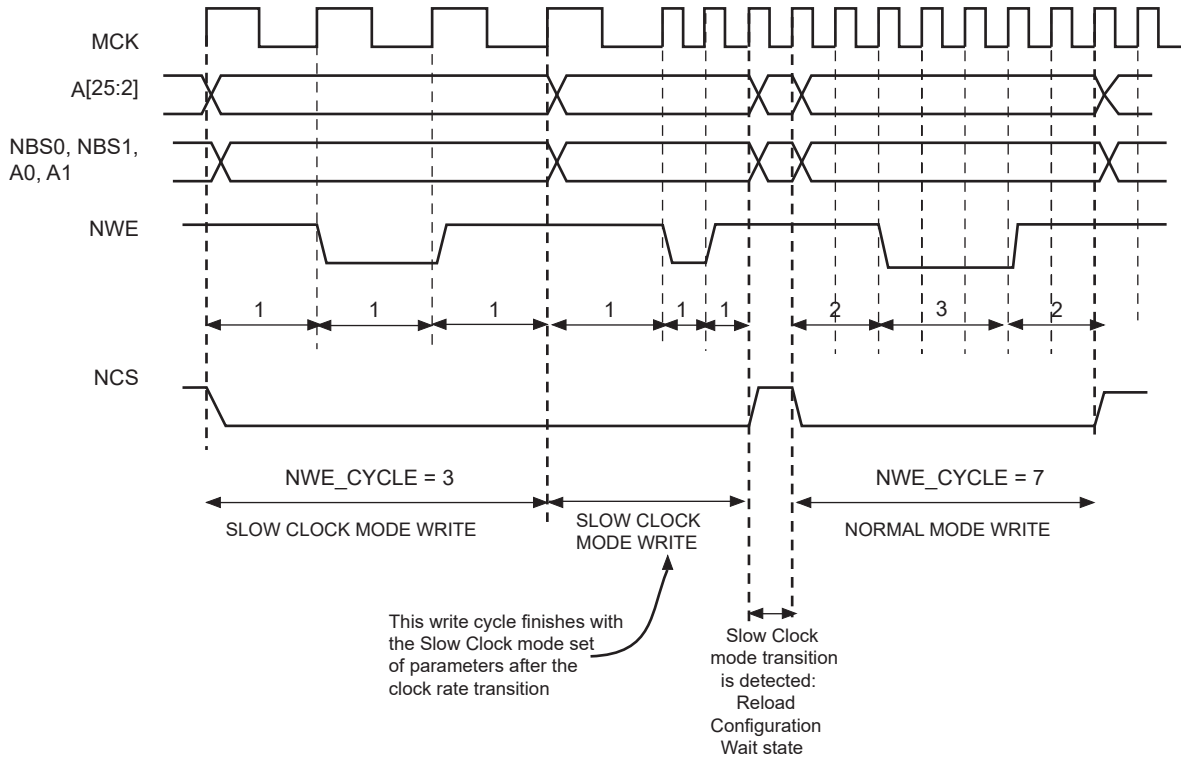


Slow Clock mode  
internal signal from PMC



NWE\_CYCLE = 3  
SLOW CLOCK MODE WRITE

SLOW CLOCK MODE WRITE

NWE\_CYCLE = 7  
NORMAL MODE WRITE

This write cycle finishes with the Slow Clock mode set of parameters after the clock rate transition

Slow Clock mode transition is detected:  
Reload Configuration  
Wait state