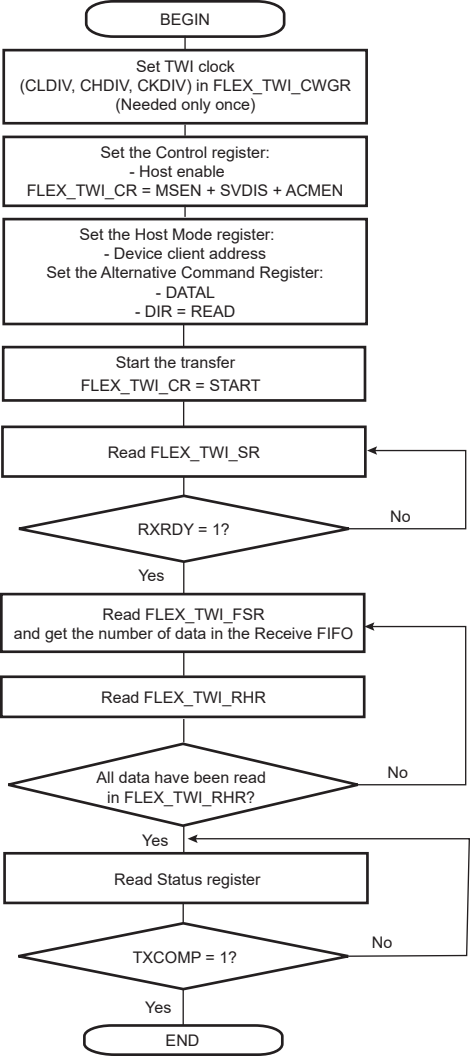
BEGIN
Set TWI clock
(CLDIV, CHDIV, CKDIV) in FLEX_TWI_CWGR
(Needed only once)
Set the Control register:
- Host enable
FLEX_TWI_CR = MSEN + SVDIS + ACMEN
Set the Host Mode register:
- Device client address
Set the Alternative Command Register:
- DATAL
- DIR = READ
Start the transfer
FLEX_TWI_CR = START
Read FLEX_TWI_SR
RXRDY = 1?
No
Yes
Read FLEX_TWI_FSR
and get the number of data in the Receive FIFO
Read FLEX_TWI_RHR
All data have been read
in FLEX_TWI_RHR?
No
Yes
Read Status register
TXCOMP = 1?
No
Yes
END