



START

No

Instruction frame with address but no data ?

Yes

Write the address in QSPI\_IAR

No

Instruction frame with instruction code and/or option code ?

Yes

Write the instruction code and/or the option code in QSPI\_ICR

Configure and send instruction frame by writing QSPI\_IFR

No

Instruction frame with data ?

Yes

Read QSPI\_IFR dummy read to synchronize peripheral and system bus accesses

No

Instruction frame with address ?

Yes

Read memory transfer TFRTYP = 1 ?

No

Yes

Read DATA via the QSPI system bus. If accesses are not sequential a new instruction is sent automatically.

Read/Write DATA via the QSPI system bus SMRM = 0 or peripheral bus SMRM = 1. The address of the first access is sent after the instruction code.

Read/Write DATA via the QSPI system bus. Address of accesses are not used by the QSPI.

Read QSPI\_SR dummy read to clear QSPI\_SR.INSTRE and QSPI\_SR.CSR.

Write QSPI\_CR.LASTXFR to 1 when all data have been transferred.

Wait for flag QSPI\_SR.INSTRE to rise by polling or interrupt.

Wait for flag QSPI\_SR.CSR to rise by polling or interrupt.

END