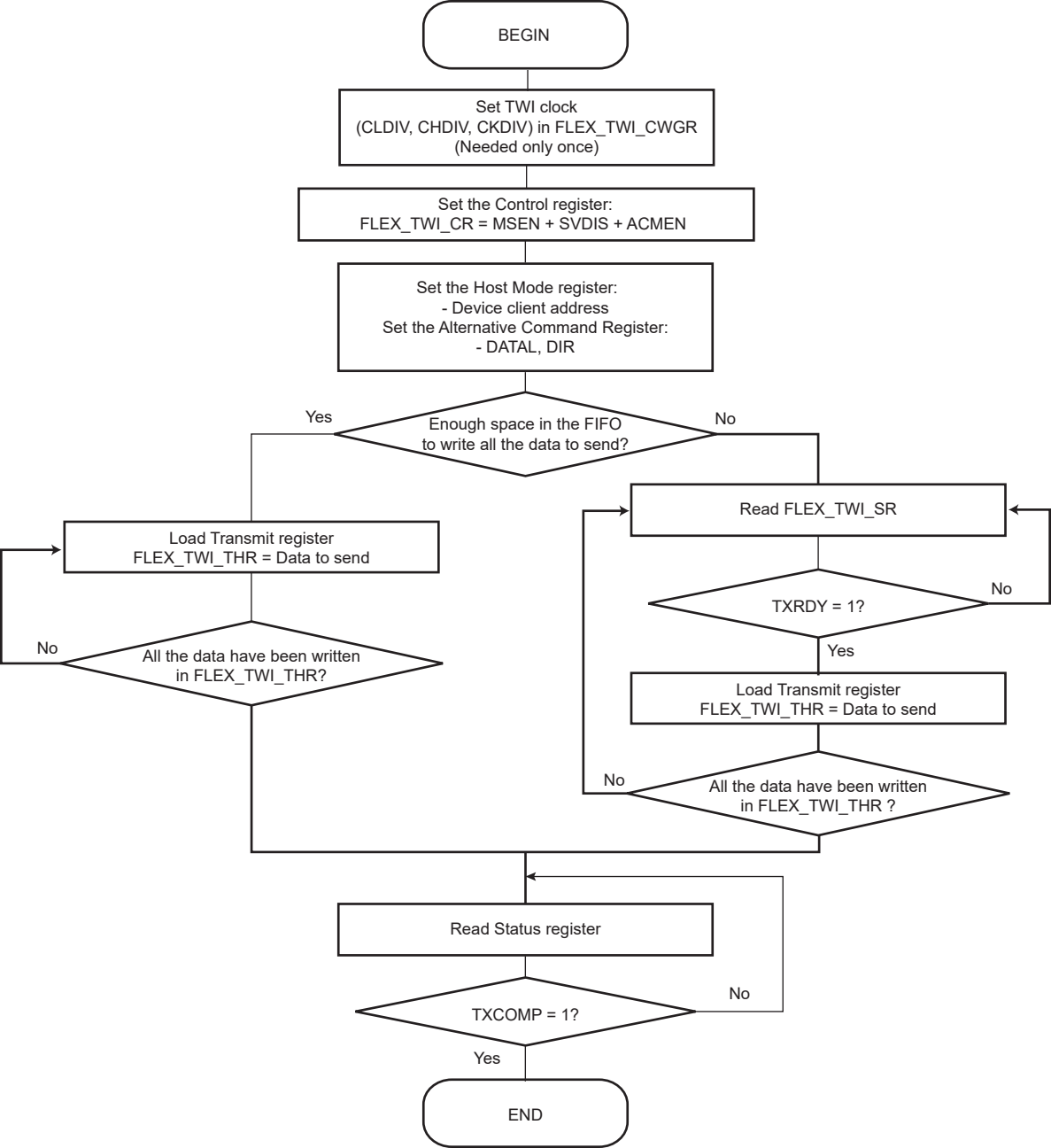
BEGIN
Set TWI clock
(CLDIV, CHDIV, CKDIV) in FLEX_TWI_CWGR
(Needed only once)
Set the Control register:
FLEX_TWI_CR = MSEN + SVDIS + ACMEN
Set the Host Mode register:
- Device client address
Set the Alternative Command Register:
- DATAL, DIR
Enough space in the FIFO
to write all the data to send?
Yes
No
Load Transmit register
FLEX_TWI_THR = Data to send
All the data have been written
in FLEX_TWI_THR?
No
Read FLEX_TWI_SR
TXRDY = 1?
No
Yes
Load Transmit register
FLEX_TWI_THR = Data to send
All the data have been written
in FLEX_TWI_THR ?
No
Read Status register
TXCOMP = 1?
No
Yes
END