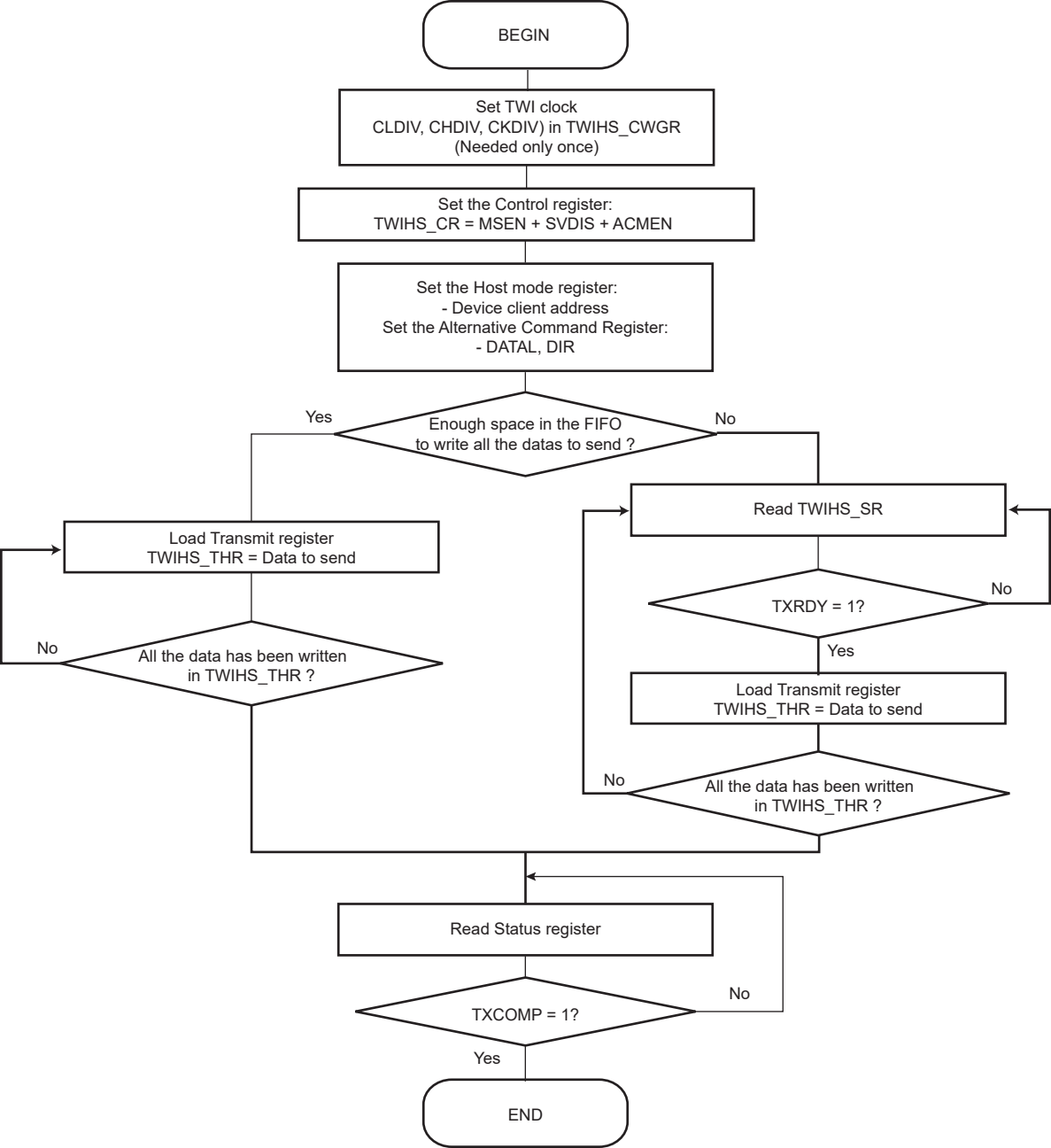
BEGIN
Set TWI clock
CLDIV, CHDIV, CKDIV) in TWIHS_CWGR
(Needed only once)
Set the Control register:
TWIHS_CR = MSEN + SVDIS + ACMEN
Set the Host mode register:
- Device client address
Set the Alternative Command Register:
- DATAL, DIR
Enough space in the FIFO
to write all the datas to send ?
Yes
No
Load Transmit register
TWIHS_THR = Data to send
All the data has been written
in TWIHS_THR ?
No
Read TWIHS_SR
TXRDY = 1?
No
Yes
Load Transmit register
TWIHS_THR = Data to send
All the data has been written
in TWIHS_THR ?
No
Read Status register
TXCOMP = 1?
No
Yes
END