

AT24C16D: Package Marking Information

<p>8-Lead PDIP</p>	<p>8-Lead SOIC</p>	<p>5-Lead SOT23</p>
<p>8-Lead TSSOP</p>	<p>8-Pad UDFN</p> <p>2.0 x 3.0 mm Body</p>	<p>8-Ball VFBGA</p> <p>1.5 x 2.0 mm Body</p>

Note 1: ● designates pin 1.

Note 2: Package drawings are not to scale.

Note 3: For SOT23 package with date codes before 7B, the bottom line (YMXX) is marked on the bottom side, and there is no Country of Assembly (@) mark on the top line.

Catalog Number Truncation		
AT24C16D		Truncation Code ###: 16D / #: AD
Date Codes		Voltages
Y = Year Code (last digit of calendar year)		% = Minimum Voltage
YY = Year Code (last 2 digits of calendar year)		M: 1.7V min
WW = Week code (week of January 1 is week '01')		
Country of Origin	Device Grade	Atmel Truncation
CO = Country of Origin	H or U: Industrial Grade	AT: Atmel ATM: Atmel ATML: Atmel
Trace Code		
NNN = Alphanumeric Trace Code (2 Characters for Small Packages)		