Address	Device		
	PIC18Fx4Q20	PIC18Fx5Q20	PIC18Fx6Q20
00 0000h to 00 3FFFh	Program Flash Memory (8KW)[1]	Program Flash Memory (16 KW)[1]	Program Flash Memory (32 KW)[1]
00 4000h to 00 7FFFh	Not Present[2]		
00 8000h to 00 FFFFh		Not Present[2]	
01 0000h to 01 FFFFh			Not Present[2]
02 0000h to 1F FFFFh			
20 0000h to 20 003Fh	User IDs (32 Words)[3]		
20 0040h to 2B FFFFh	Reserved		
2C 0000h to 2C 00FFh	Device Information Area (DIA)[3,5]		
2C 0100h to 2F FFFFh	Reserved		
30 0000h to 30 0019h	Configuration Bytes[3]		
30 001Ah to 37 FFFFh	Reserved		
38 0000h to 38 00FFh	Data EEPROM (256 Bytes)		
38 0100h to 3B FFFFh	Reserved		
3C 0000h to 3C 0008h	Device Configuration Information[3,4,5]		
3C 0009h to 3F FFFBh	Reserved		
3F FFFCh to 3F FFFDh	Revision ID (1 Word)[3,4,5]		
3F FFFEh to 3F FFFFh	Device ID (1 Word)[3,4,5]		

Note 1: A configurable Storage Area Flash is implemented as part of the User Flash, if enabled.

2: The addresses do not roll over. The region is read as '0'.

3: Not code-protected.

4: Hard-coded in silicon.

5: This region cannot be written by the user and it's not affected by a Bulk Erase.