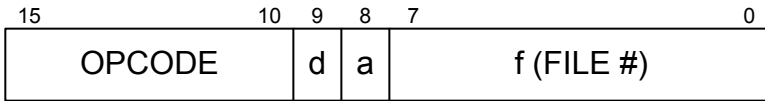


Byte-oriented file register operations



d = 0 for result destination to be WREG register

d = 1 for result destination to be file register (f)

a = 0 to force Access Bank

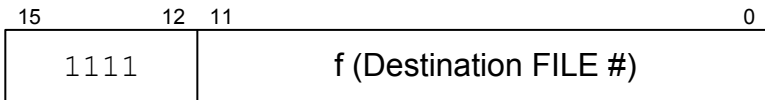
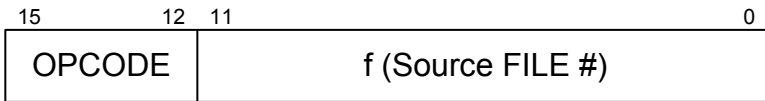
a = 1 for BSR to select bank

f = 8-bit file register address

Example Instruction

```
ADDWF MYREG, W, B
```

Byte to Byte move operations (two-word)

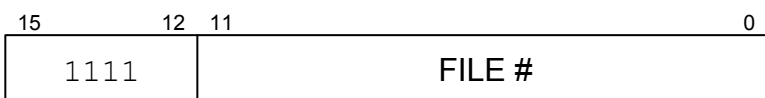
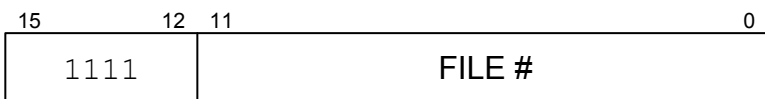


f = 12-bit file register address

Example Instruction

```
MOVFF MYREG1, MYREG2
```

Byte to Byte move operations (three-word)



Example Instruction

```
MOVFFL MYREG1, MYREG2
```