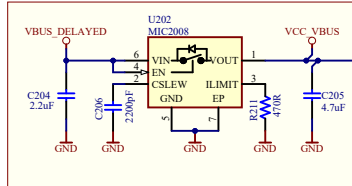


MIC5353:
 V_{in} : 2.6V to 6V
 V_{out} : 1.25V to 5.1V
 I_{max} : 500mA
 Dropout (typical): 50mV @ 150mA, 160mV @ 500mA
 Accuracy: 2% initial
 Thermal shutdown and current limit

Maximum output voltage is limited by the input voltage and the dropout voltage in the regulator. ($V_{max} = V_{in} - \text{dropout}$)

Interface Signal	ICSP™ TARGET	UPDI TARGET	SWD TARGET
CDC TX	UART RX	UART RX	UART RX
CDC RX	UART TX	UART TX	UART TX
DBG0	DAT	UPDI	SWDAT
DBG1	CLK	GPIO	SWCLK
DBG2	GPIO	GPIO	SWO/GPIO
DBG3	<u>MCLR</u>	<u>RESET</u>	<u>RESET</u>
VCC	-	-	-

VBUS SLEW RATE- & CURRENT-LIMIT



CURRENT LIMIT:
- With R211 set to 470 ohms, the current through the power switch MIC2008 is limited to 500mA.

SLEW RATE LIMIT:
- The CSLEW feature is implemented incorrectly. CSLEW may be connected to VIN with a capacitor. The actual slew rate for VCC_VBUS is approximately 50V/ms.

MIC5528:
 Vin: 2.5V to 5.5V
 Vout: Fixed 3.3V
 Imax: 500mA
 Dropout: 260mV @ 500mA

The diagram shows the J203 connector (1-1xLS1-5409-R) with the following connections:

- A1/B12** and **A4/B9** are connected to a common signal line.
- A6/B6** and **A7/B7** are connected to a common signal line.
- A8/B8** is connected to a common signal line.
- SHIELD** pins (1, 2, 3, 4) are connected to GND.
- SBU2** (CC2) and **SBU1** (CC1) are connected to GND via resistors **R2** (16 ohms) and **R3** (5.1k ohms) respectively.

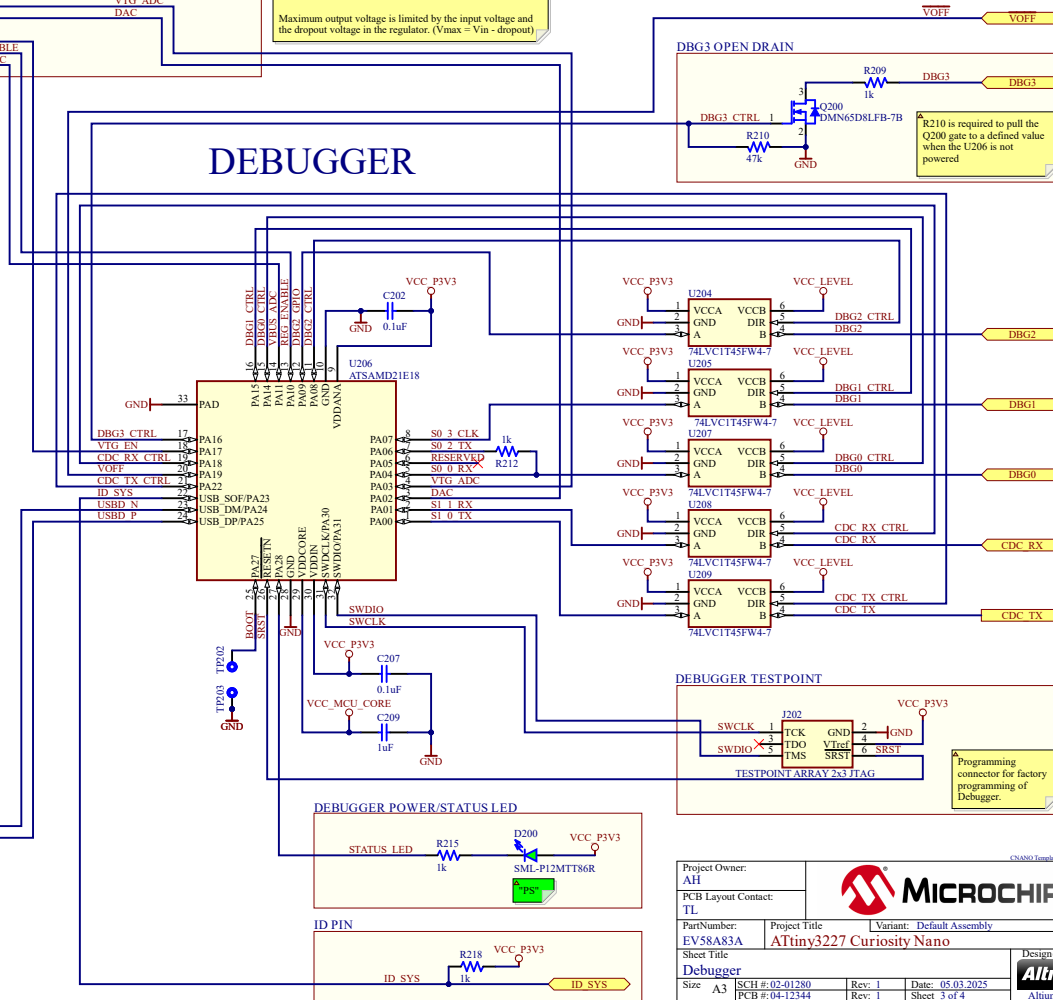
START-UP DELAY::
With $C_{208} = 0.1\mu F$ and $R_{214} = 47k\Omega$, the P-MOS is delaying the turn-on of V_{CC} DELAYED by 1ms, compared to V_{BUS} .

The diagram shows the J203 connector (1-1xLS1-5409-R) with the following connections:

- A1/B12** and **A4/B9** are connected to a common signal line.
- A6/B6** and **A7/B7** are connected to a common signal line.
- A8/B8** is connected to a common signal line.
- SHIELD** pins (1, 2, 3, 4) are connected to GND.
- SBU2** (CC2) and **SBU1** (CC1) are connected to GND via resistors **R2** (16 ohms) and **R3** (5.1k ohms) respectively.

TP200 and TP201 are MTG Holes in the corners of the PCB close to the DEBUGGER section labelled "GND" on the silkscreen.

DEBUGGER



CNANO Template Revision: 2.7

Project Owner: AH		 MICROCHIP		CS4000 Template Revision 2.1	
PCB Layout Contact: TL					
PartNumber: EV58A33A		Project Title ATinyT3227 Curiosity Nano		Variant: <u>Default Assembly</u>	
Sheet Title Debugger					
Size: A3		SCH #02-01280 PCB #06-12345		Rev: 1 Date: 05.03.2025 Rev: 1 Sheet 3 of 4	
Files: A3m3277_Curiosity_Nano_Debugger...					
					