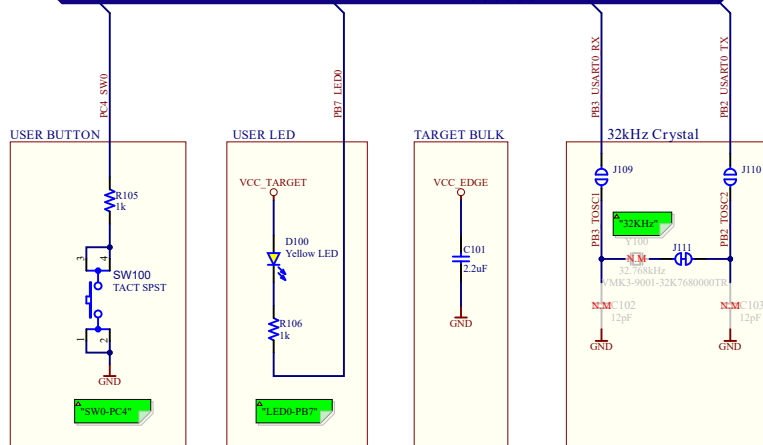
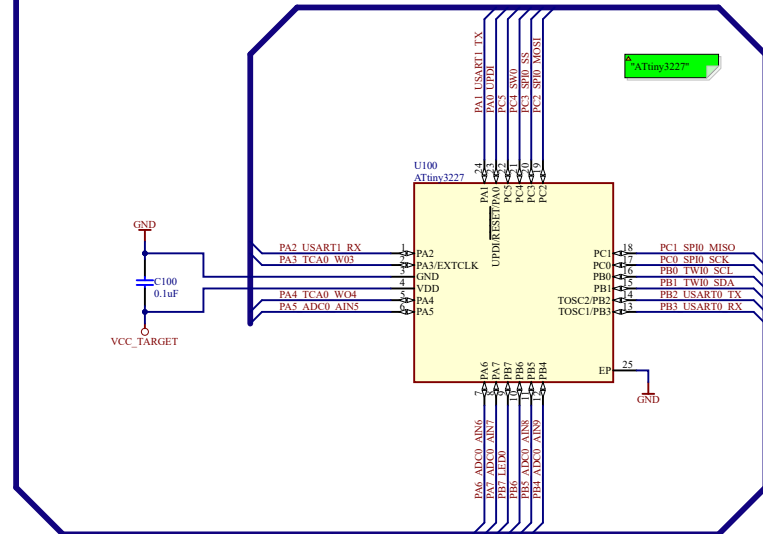
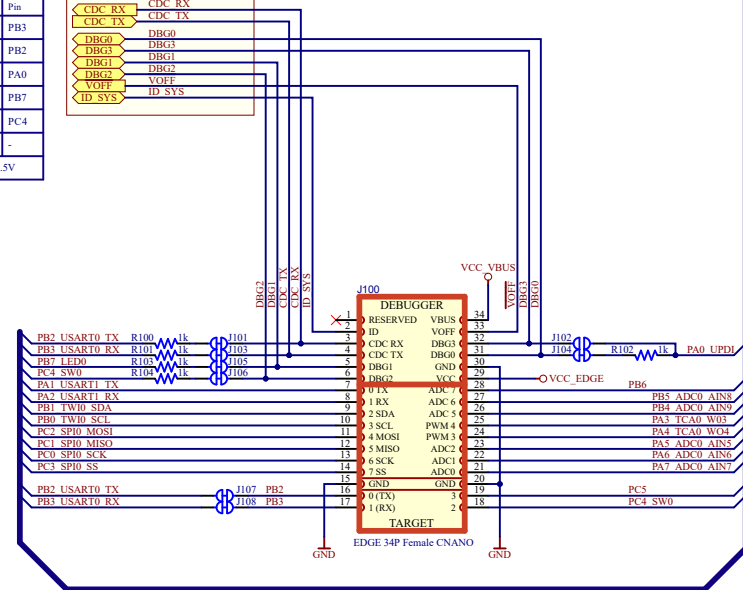


ATtiny3227



ATtiny3227		
Debugger	Name	Pin
CDC TX	USART0 RX	PB3
CDC RX	USART0 TX	PB2
DBG0	UPDI	PA0
DBG1	GPIO1	PB7
DBG2	GPIO0	PC4
DBG3	NA	-
VTG	1.8V - 5.5V	

DEBUGGER CONNECTIONS



NOTE on I2C:
No pull-ups on board. Pull-ups must be mounted close to client device(s).

NOTE on UART/CDC:
RX/TX on the header denotes the input/output direction of the signal respective to it's source.
CDC TX is output from the DEBUGGER.
CDC RX is input to the DEBUGGER.
TX is output from the TARGET device.
RX is input to the TARGET device.

NOTE on Crystal:
The crystal is optional and not populated by default

Crystal datasheet:
Crystal = 9pF
max ESR = 70kOhm
Accuracy ±20ppm
ATtiny3227 datasheet:
C_{xin} = 5pF
C_{load} = 5pF
C_L = 1/(1/(5pF) + (1/(5pF))) ≈ 2.5pF
Maximum Load = 12.5pF
Estimated C_{pcb} = 0.5pF
Estimated load
C = 2 (C_{Crystal} - C_{para} - C_{pcb})
C = 2 (9pF - 2.5pF - 0.5pF)
C = 12pF

Project Owner:
AH

PCB Layout Contact:
TL

PartNumber:
EV58A83A

Sheet Title:
Target MCU

Size:
A3

Project Title:
ATtiny3227 Curiosity Nano

Variant: Default Assembly

SCH #: 02-01280
Rev: 1

Date: 20.01.2025

Rev: 1

Sheet 2 of 4

Designed with
Altium

File: ATtiny3227 Curiosity Nano Target MCU SchDoc

CSANO Toplevel Revision 3.2

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