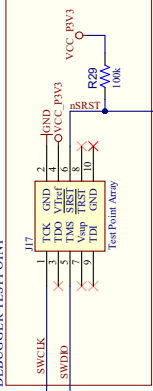


DEBUGGER

Interface	SWD	TARGET
Signal	CDC TX	UART TX
	CDC RX	UART RX
	DRG0	SWDAT
	DRG1	SWCLK
	DRG2	GPIO
	DRG3	RESET
	VCC	3.3V

DEBUGGER TESTPOINT



DEBUGGER REGULATOR

