

CRC-16-ANSI

$$x^{16} + x^{15} + x^2 + 1 \text{ (17 bits)}$$

Standard 16-bit representation = 0x8005

CRCXORH = 0b10000000

CRCXORL = 0b0000010- (1)

Data Sequence:

0x55, 0x66, 0x77, 0x88

DLEN = 0b0111

PLEN = 0b1111

Data entered into the CRC:

SHIFTM = 0:

01010101 01100110 01110111 10001000

SHIFTM = 1:

10101010 01100110 11101110 00010001

Check Value (ACCM = 1):

SHIFTM = 0: 0x32D6

CRCACCH = 0b00110010

CRCACCL = 0b11010110

SHIFTM = 1: 0x6BA2

CRCACCH = 0b01101011

CRCACCL = 0b10100010

Note 1: Bit 0 is unimplemented. The LSb of any CRC polynomial is always '1' and will always be treated as a '1' by the CRC for calculating the CRC check value. This bit will be read in software as a '0'.