


BANK 62

1F00h	Core Registers
1F0Bh	
1F0Ch	—
1F0Dh	—
1F0Eh	—
1F0Fh	—
1F10h	RAOPPS
1F11h	RA1PPS
1F12h	RA2PPS
1F13h	RA3PPS ⁽²⁾
1F14h	RA4PPS
1F15h	RA5PPS
1F16h	Reserved
-	
1F1Bh	
1F1Ch	RB4PPS ⁽¹⁾
1F1Dh	RB5PPS ⁽¹⁾
1F1Eh	RB6PPS ⁽¹⁾
1F1Fh	RB7PPS ⁽¹⁾
1F20h	RCOPPS
1F21h	RC1PPS
1F22h	RC2PPS
1F23h	RC3PPS
1F24h	RC4PPS
1F25h	RC5PPS
1F26h	RC6PPS ⁽¹⁾
1F27h	RC7PPS ⁽¹⁾
1F28h	Reserved
-	
1F37h	
1F38h	ANSELA
1F39h	WPUA
1F3Ah	ODCONA
1F3Bh	SLRCONA
1F3Ch	INLVLA
1F3Dh	IOCAP
1F3Eh	IOCAN
1F3Fh	IOCAF
1F40h	—
1F41h	—
1F42h	—
1F43h	ANSELB ⁽¹⁾
1F44h	WPUB ⁽¹⁾
1F45h	ODCONB ⁽¹⁾
1F46h	SLRCONB ⁽¹⁾

1F47h	INLVLB ⁽¹⁾
1F48h	IOCBP ⁽¹⁾
1F49h	IOCBN ⁽¹⁾
1F4Ah	IOCBF ⁽¹⁾
1F4Bh	—
1F4Ch	—
1F4Dh	—
1F4Eh	ANSELC
1F4Fh	WPUC
1F50h	ODCONC
1F51h	SLRCONC
1F52h	INLVLC
1F53h	IOCCP
1F54h	IOCCN
1F55h	IOCCF
1F56h	Reserved
-	
1F6Fh	
1F70h	Common RAM (Accesses 70h-7Fh)
1F7Fh	

Note 1: Available on PIC16F15243/44 only.

Legend:

 Unimplemented data memory locations, read as '0'