

0x0000 (0x01)
on_off_config (0x02)
clear_faults (0x03)
write_protect (0x10)
store_user_all (0x15)
restore_user_all (0x16)
capability (0x19)
smba_alert_mask (0x1B)
vout_command (0x21)
vout_max (0x24)
vout_margin_high (0x25)
vout_margin_low (0x26)
vout_transition_rate (0x27)
vin_on (0x35)
vin_off (0x36)
vout_ov_fault_limit (0x40)
vout_ov_fault_response (0x41)
vout_ov_warm_limit (0x42)
vout_uv_warm_limit (0x43)
vout_uv_fault_limit (0x44)
vout_uv_fault_response (0x45)
iout_oc_fault_limit (0x46)
iout_oc_fault_response (0x47)
vin_ov_fault_limit (0x55)
vin_ov_fault_response (0x56)
vin_uv_warm_limit (0x58)
power_good_on (0x5E)
ton_delay (0x60)
ton_rise (0x61)
ton_max_fault_limit (0x62)
ton_max_fault_response (0x63)
toff_delay (0x64)
toff_fall (0x65)
status_byte (0x78)
status_word (0x79)
status_vout (0x7A)
status_iout (0x7B)
status_input (0x7C)
status_temperature (0x7D)
status_cml (0x7E)
read_vin (0x88)
read_vout (0x8B)
read_temperature (0x8D)
pmbus_revision (0x98)
mfr_id (0x99)
mfr_model (0x9A)
mfr_revision (0x9B)
ic_device_id (0x9D)

7	6	5	4	3	2	1	0	
0	0	0	0	0	0	0	0	<-> 0x 00
Out	Po	Voltage	Margin	Tra	Res			

04/15/2025 02:51:01.778 PM: ===== Reading vout_report_lower, vout_report_upper =====
04/15/2025 02:51:01.788 PM: Reading: register vout_report_lower = 0x3E
04/15/2025 02:51:01.818 PM: Reading: register vout_report_upper = 0x00
04/15/2025 02:51:01.818 PM: ===== Reading: vout_report_lower = 0x8E, vout_report_upper = 0x00 DONE! =====
04/15/2025 02:51:01.818 PM: ===== Reading addr_report_lower, addr_report_upper =====
04/15/2025 02:51:01.840 PM: Reading: register addr_report_lower = 0x00
04/15/2025 02:51:01.870 PM: Reading: register addr_report_upper = 0x00
04/15/2025 02:51:01.870 PM: ===== Reading: addr_report_lower = 0x00, addr_report_upper = 0x00 DONE! =====
04/15/2025 02:51:01.870 PM: ===== Reading vcc_report_lower, vcc_report_upper =====
04/15/2025 02:51:01.892 PM: Reading: register vcc_report_lower = 0xA3
04/15/2025 02:51:01.911 PM: Reading: register vcc_report_upper = 0xD8
04/15/2025 02:51:01.911 PM: ===== Reading: vcc_report_lower = 0xA3, vcc_report_upper = 0xD8 DONE! =====