

AT28HC256: Package Marking Information

32-Lead PLCC

Topside

●

ATMEL
AT28HC256#
%%U-19802C
YYWWNNN

Backside

28-Lead SOIC

Topside

ATMEL 19802C
28HC256#-%%U
YYWWNNN

●

Backside

Note: no backside markings

| | | | |
|--|--|---|--|
| | | %% = Access Time | |
| | | 70: 70 ns 90: 90 ns 12: 120 ns | |
| # Options | | Lot Trace Code | |
| Blank = 10k writes, 10ms standard E = 100k writes, Extended endurance F = 3ms Fast write cycle | | YYWWNNN: Lot Trace Code YY = Year, WW = Work Week NNN = Assembly Trace Code | |
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