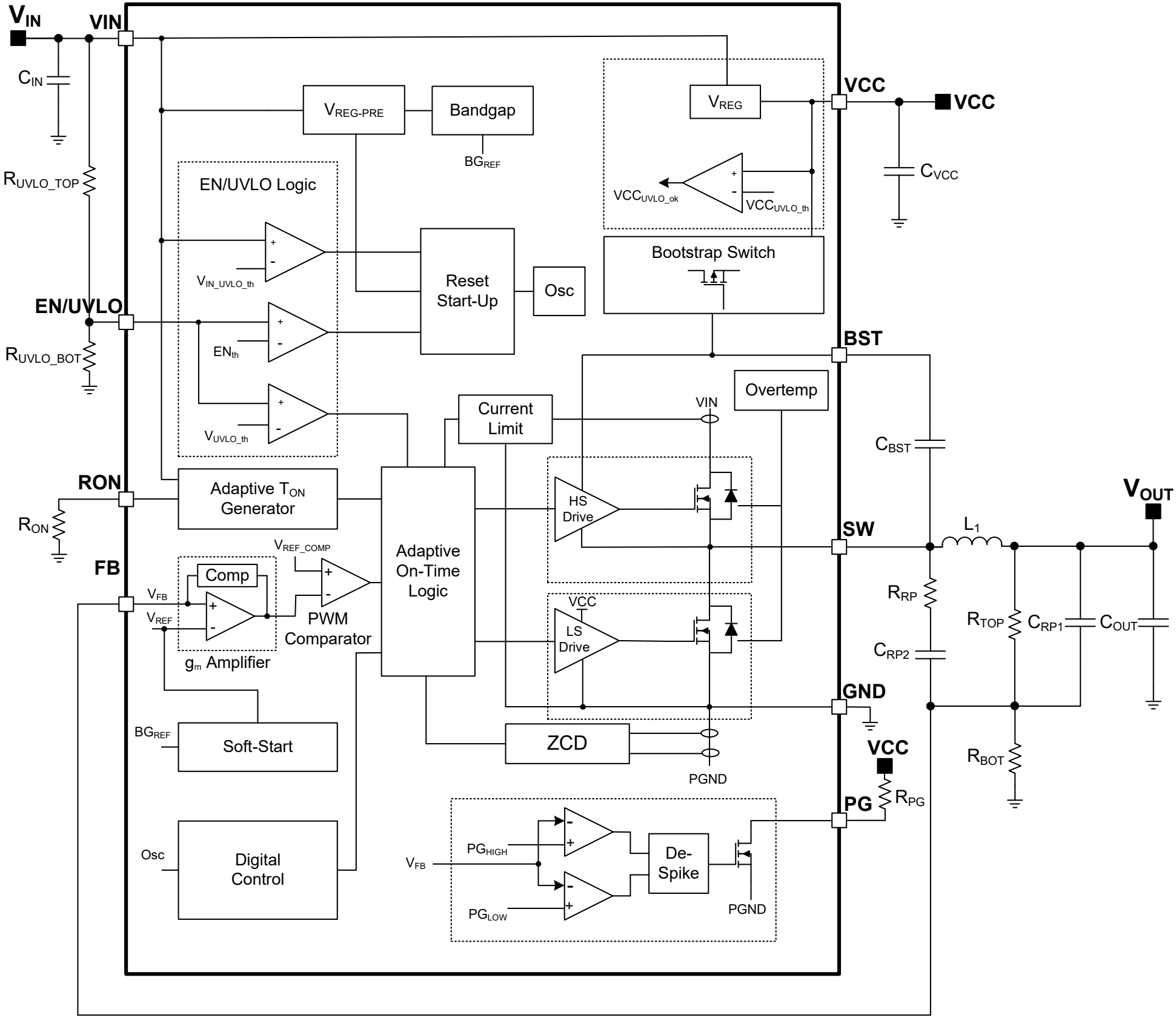

VIN
VIN
CIN
RUVLO_TOP
EN/UVLO
RUVLO_BOT
RON
RON
FB
VREG-PRE
Bandgap
BGREF
EN/UVLO Logic
VIN_UVLO_th
ENth
VUVLO_th
Reset
Start-Up
Osc
VREG
VCC
VCC
CVCC
VCCUVLO_ok
VCCUVLO_th
Bootstrap Switch
BST
Overtemp
Current
Limit
VIN
Adaptive TON
Generator
Adaptive
On-Time
Logic
HS
Drive
LS
Drive
VCC
SW
CBST
VOUT
L1
VREF_COMP
Comp
VFB
VREF
gm Amplifier
PWM
Comparator
RRP
RTOP
CRP1
COUT
CRP2
GND
BGREF
Soft-Start
ZCD
PGND
VCC
RPG
RBOT
PG
Osc
Digital
Control
VFB
PGHIGH
PGLOW
De-
Spike
PGND