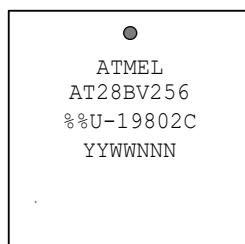


AT28BV256: Package Marking Information

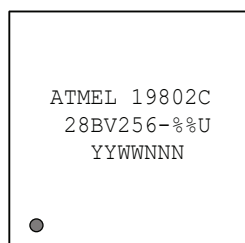
32-Lead PLCC

Topside



28-Lead SOIC

Topside



Note: No backside marking on available packages

	%% = Access Time		
	20: 200ns		
		Lot Trace Code	
		YYWWNNN: Lot Trace Code YY: Year, WW: Work Week, NNN: Assembly Trace Code	