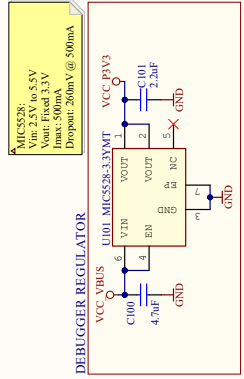


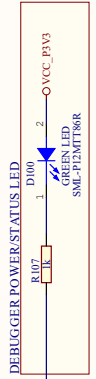
Interface Signal	ICSP TARGET	UPDI TARGET
CDC TX	UART TX	UART TX
CDC RX	UART TX	UART TX
DBG0	DAT	UPDI
DBG1	CLK	GPO
DBG2	GPO	GPO
DBG3	MCLR	RESET
VCC	-	-

J100:
 - Cut-strap used for full separation of target power from the level shifters and on-board regulators.
 - For current measurements using the on-board power supply, this strap must be cut and an external power supply used.
 - For current measurements using an external power supply, this strap could be cut for more accurate measurements. Leakage back through the switch is in the micro ampere range.

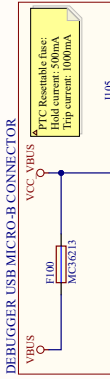
MIC5353:
 Vin: 2.6V to 6V
 Vout: 0.5V to 1.1V
 Iout: 500mA
 Dropout (typical): 50mV @ 150mA, 160mV @ 500mA
 Accuracy: 2% initial
 Thermal shutdown and current limit
 Maximum output voltage is limited by the input voltage and the dropout voltage in the regulator.
 (Vmax = Vin - dropout)



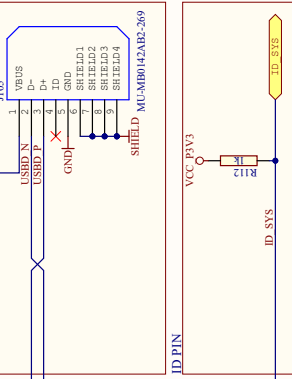
DEBUGGER REGULATOR



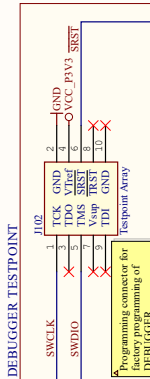
DEBUGGER POWER STATUS LED



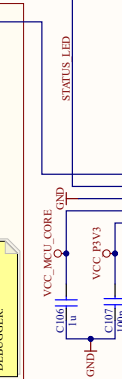
DEBUGGER USB MICRO-B CONNECTOR



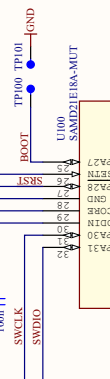
ID PIN



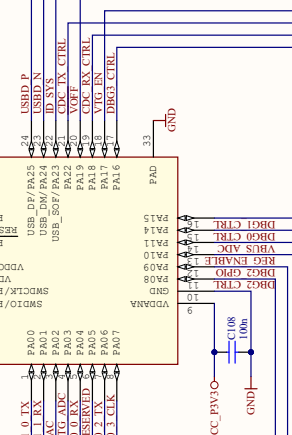
DEBUGGER TESTPOINT



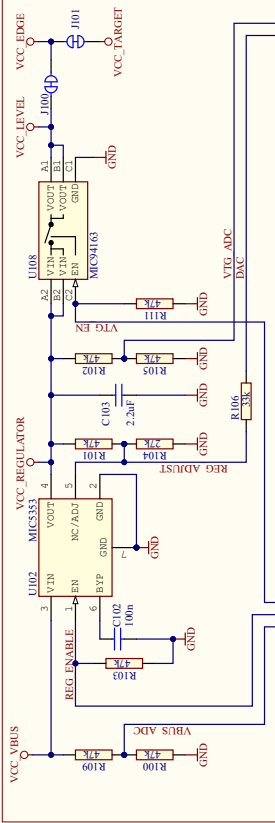
VCC_MCU_CORE



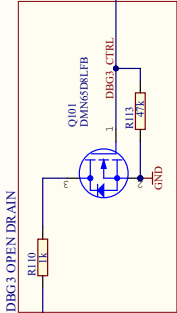
STATUS LED



Adjustable output and limitations:
 - The DEBUGGER can adjust the output voltage of the regulator between 1.25V and 5.1V to the target.
 - The level shifters have a minimal voltage level of 1.65V and will limit the minimum operating voltage allowed for the target to still allow communication.
 - The MIC94163 has a minimal voltage level of 1.70V and will limit the minimum voltage delivered to the target.
 - Firmware configuration will limit the voltage range to be within the target specification.



TARGET ADJUSTABLE REGULATOR



DBG3 OPEN DRAIN