

Address	Device	
	PIC18Fx6Q83/84	PIC18Fx7Q83/84
00 0000h to 00 3FFFh	Program Flash Memory (32 KW) ⁽¹⁾	Program Flash Memory (64 KW) ⁽¹⁾
00 4000h to 00 7FFFh		
00 8000h to 00 FFFFh		
01 0000h to 01 FFFFh	Not Present ⁽²⁾	Not Present ⁽²⁾
02 0000h to 1F FFFFh		
20 0000h to 20 001Fh	User IDs (32 Words) ⁽³⁾	
20 0020h to 2B FFFFh	Reserved	
2C 0000h to 2C 00FFh	Device Information Area (DIA) ^(3,5)	
2C 0100h to 2F FFFFh	Reserved	
30 0000h to 30 0022h	Configuration Bytes ⁽³⁾	
30 0023h to 37 FFFFh	Reserved	
38 0000h to 38 03FFh	Data EEPROM (1024 Bytes)	
38 0400h to 3B FFFFh	Reserved	
3C 0000h to 3C 000Ah	Device Configuration Information ^(3,4,5)	
3C 000Bh to 3F FFFBh	Reserved	
3F FFFCh to 3F FFFDh	Revision ID (1 Word) ^(3,4,5)	
3F FFFEh to 3F FFFFh	Device ID (1 Word) ^(3,4,5)	

Notes: 1. Storage Area Flash is implemented as the last 128 Words of User Flash, if enabled.

2. The addresses do not roll over. The region is read as '0'.

3. Not code-protected.

4. Hard-coded in silicon.

5. This region cannot be written by the user and it is not affected by a Bulk Erase.