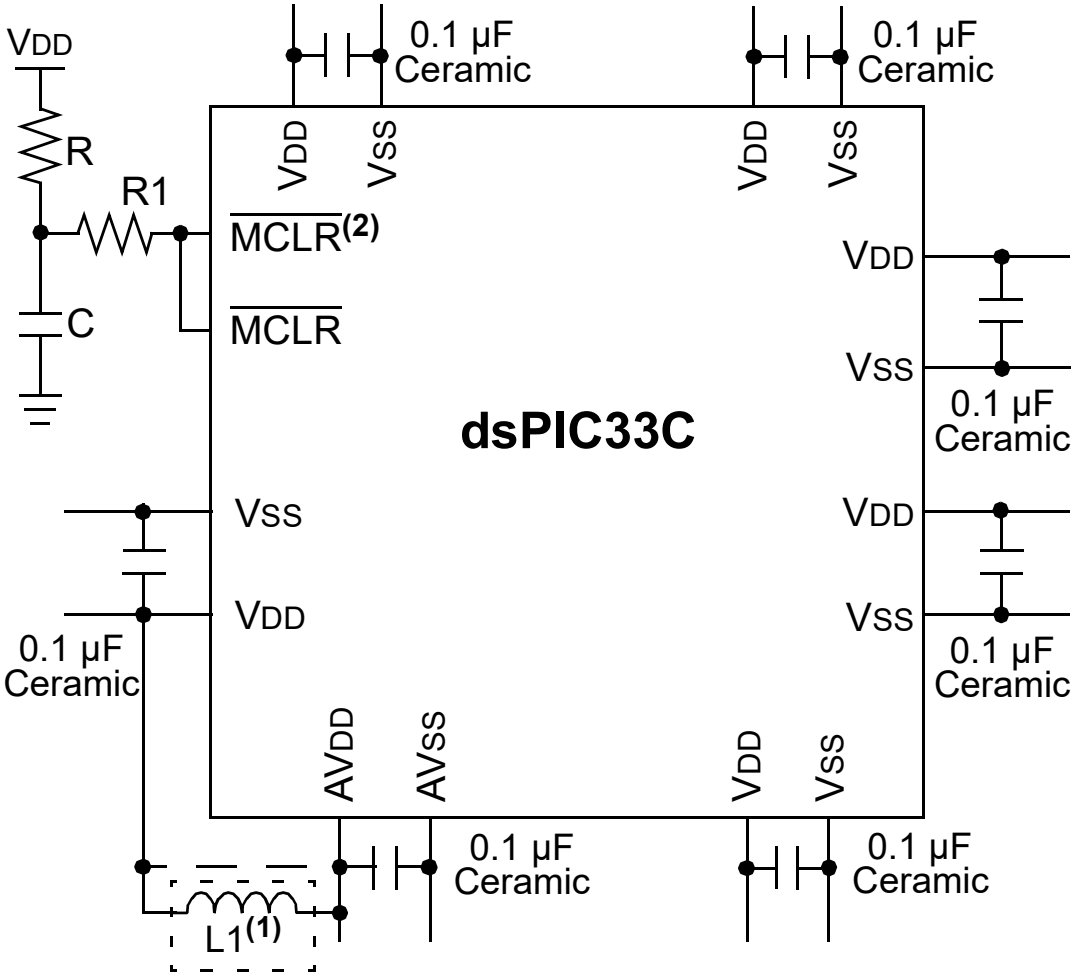

Note 1: As an option, instead of a hard-wired connection, an inductor (L1) can be substituted between V_{DD} and AV_{DD} to improve ADC noise rejection. The inductor impedance should be less than 1Ω and the inductor capacity greater than 10 mA.

2: This device has two $\overline{MCLR}$ pins.

Where:

$$f = \frac{F_{CNV}}{2}$$ (i.e., ADC conversion rate/2)

$$f = \frac{1}{(2\pi\sqrt{LC})}$$

$$L = \left(\frac{1}{(2\pi f\sqrt{C})}\right)^2$$