

Start Erase Operation

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graph TD; Start([Start Erase Operation]) --> Load[Load the NVMADR register with address in the page to be erased]; Load --> SetCmd[Set NVM Command to erase (NVMCMD = 'b110)]; SetCmd --> DisableInt[Disable interrupts (GIE = 0)]; DisableInt --> Unlock[Execute unlock sequence including setting the GO bit]; Unlock --> Stall[CPU stalls while erase executes]; Stall --> EnableInt[Enable interrupts (GIE = 1)]; EnableInt --> ClearCmd[Clear NVM Command (NVMCMD = 'b000)]; ClearCmd --> End([End Erase Operation]);
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Load the NVMADR register with address in the page to be erased

Set NVM Command to erase
(NVMCMD = 'b110)

Disable interrupts
(GIE = 0)

Execute unlock sequence
including setting the GO bit

CPU stalls while erase executes

Enable interrupts
(GIE = 1)

Clear NVM Command
(NVMCMD = 'b000)

End Erase Operation