Address	Device		
	PIC18Fx4Q40	PIC18Fx5Q40	PIC18Fx6Q40
00 0000h to 00 3FFFh	Program Flash Memory (8KW)[1]	Program Flash Memory (16 KW)[1]	Program Flash Memory (32 KW)[1]
00 4000h to 00 7FFFh	Not Present[2]		
00 8000h to 00 FFFFh		Not Present[2]	
01 0000h to 01 FFFFh			Not Present[2]
02 0000h to 1F FFFFh			
20 0000h to 20 003Fh	User IDs (32 Words)[3]		
20 0040h to 2B FFFFh	Reserved		
2C 0000h to 2C 00FFh	Device Information Area (DIA)[3,5]		
2C 0100h to 2F FFFFh	Reserved		
30 0000h to 30 0009h	Configuration Bytes[3]		
30 000Ah to 37 FFFFh	Reserved		
38 0000h to 38 01FFh	Data EEPROM (512 Bytes)		
38 0200h to 3B FFFFh	Reserved		
3C 0000h to 3C 0009h	Device Configuration Information[3,4,5]		
3C 000Ah to 3F FFFBh	Reserved		
3F FFFCh to 3F FFFDh	Revision ID (1 Word)[3,4,5]		
3F FFFEh to 3F FFFFh	Device ID (1 Word)[3,4,5]		

Notes: 1. Storage Area Flash is implemented as the last 128 Words of User Flash, if enabled.

2. The addresses do not roll over. The region is read as '0'.

3. Not code-protected.

4. Hard-coded in silicon.

5. This region cannot be written by the user and it is not affected by a Bulk Erase.