

## Active Channels List

Channel Number	Trigger
DMAC Channel 0	SPI0_Transmit
DMAC Channel 1	SPI0_Receive

Add Channel

Remove Selected Channel

☐ Use Linked List Mode

## DMA Channel 0 Settings

Enable Interrupt



Source Addressing Mode

Increment Address After Every Transfer

Destination Addressing Mode

Fixed Address Mode

DMA Interface Bus To Read Source Data

DMA Interface Bus 0

DMA Interface Bus To Write Destination Data

DMA Interface Bus 1

Data Width

8-Bits

Data Transfers Per DMA Request

1 Transfer Per Request

Burst Size For Memory To Memory Transfer

1 Transfer Per Burst