

```
60
61 int main ( void )
62 {
63     /* Initialize all modules */
64     SYS_Initialize ( NULL );
65
66     XDMAC_ChannelCallbackRegister(XDMAC_CHANNEL_0, TX_DMA_EventHandler, (uintptr_t)NULL);
67     XDMAC_ChannelCallbackRegister(XDMAC_CHANNEL_1, RX_DMA_EventHandler, (uintptr_t)NULL);
68
69     DCACHE_INVALIDATE_BY_ADDR((uint32_t *)tx, 10);
70     DCACHE_INVALIDATE_BY_ADDR((uint32_t *)rx, 10);
71
72     XDMAC_ChannelTransfer(XDMAC_CHANNEL_1, (void *)&SPI0_REGS->SPI_RDR, rx, sizeof(rx));
73     XDMAC_ChannelTransfer(XDMAC_CHANNEL_0, tx, (void *)&SPI0_REGS->SPI_TDR, sizeof(tx));
74
75     while ( true )
76     {
77         /* Maintain state machines of all polled MPLAB Harmony modules. */
78         SYS_Tasks ( );
79     }
80
81     /* Execution should not come here during normal operation */
82
83     return ( EXIT_FAILURE );
84 }
85
```