	Name: Address:	RCxSTA 0xF9C,0xE98						
ı	Receive Status and Control Register							
Bit	7	6	5	4	3	2	1	0
	SPEN	RX9	SREN	CREN	ADDEN	FERR	OERR	RX9D
Access	R/W	R/W	R/W	R/W	R/W	RO	R/HC	R/HC
Reset	0	0	0	0	0	0	0	0
Bit 7 – SPEN Serial Port Enable bit								
	Value	Description						
	1	Serial port enabled						
	0	Serial port disabled (held in Reset)						