

## 17.9.1 Peripheral xxx Input Selection

**Name:** xxxPPS

Bit	7	6	5	4	3	2	1	0
				PORT[1:0]		PIN[2:0]		
Access				R/W	R/W	R/W	R/W	R/W
Reset				g	g	g	g	g

**Bits 4:3 – PORT[1:0]** Peripheral xxx Input PORT Selection bits

See the input selection table for a list of available ports and default pin locations.

Value	Description
11	PORTD
10	PORTC
01	PORTB
00	PORTA

**Bits 2:0 – PIN[2:0]** Peripheral xxx Input Pin Selection bits

Value	Description
111	Peripheral input is from PORTx Pin 7 (Rx7)
110	Peripheral input is from PORTx Pin 6 (Rx6)
101	Peripheral input is from PORTx Pin 5 (Rx5)
100	Peripheral input is from PORTx Pin 4 (Rx4)
011	Peripheral input is from PORTx Pin 3 (Rx3)
010	Peripheral input is from PORTx Pin 2 (Rx2)
001	Peripheral input is from PORTx Pin 1 (Rx1)
000	Peripheral input is from PORTx Pin 0 (Rx0)