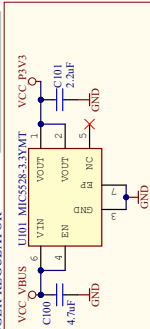


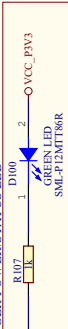
Interface	ICSP	TARGET	SWD	TARGET
Signal	CDCTX	UART RX	UART RX	UART RX
	CDCRX	UART TX	UART TX	UART TX
	DRG0	DAT	SWDAT	SWDAT
	DRG1	CLK	GPO	SWCLK
	DRG2	GPO	SWOGPO	SWOGPO
	DRG3	MCUR	RESET	RESET
	VCC	-	-	-

MIC5238
Vin: 5V to 5.5V
Vout: Fixed 1.3V
Imax: 500mA
Dropout: 20mV @ 500mA

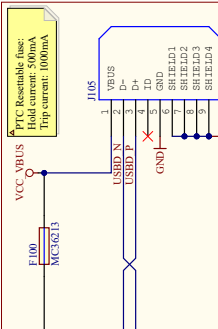
DEBUGGER REGULATOR



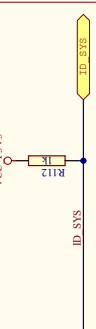
DEBUGGER POWER/STATUS LED



DEBUGGER USB MICRO-B CONNECTOR



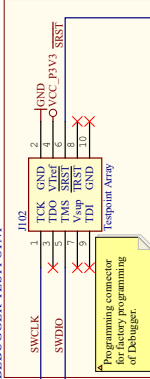
ID PIN



J100
- Cut-strap used for full separation of target power from the level shifters and on-board regulators.
- For current measurements using an external power supply, this strap could be cut for more accurate measurements. Leakage back through the switch in the in-circuit amperage range.
- For current measurements using the on-board power supply, this strap must be cut and an ammeter connected across.

MIC5353:
Vin: 2.6V to 6V
Vout: 1.25V to 5.1V
Imax: 500mA
Dropout (typical): 5mV @ 150mA, 16mV @ 500mA
Accuracy: 2% initial
Thermal shutdown and current limit
Maximum output voltage is limited by the input voltage and the dropout voltage in the regulator.
(Vmax = Vin - dropout)

DEBUGGER TESTPOINT



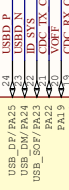
STATUS LED



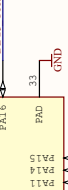
BOOT



SWDIO



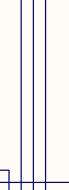
SWCLK



VDDANA



REG_ENABLE



DBG1_CTRL



DBG2_CTRL

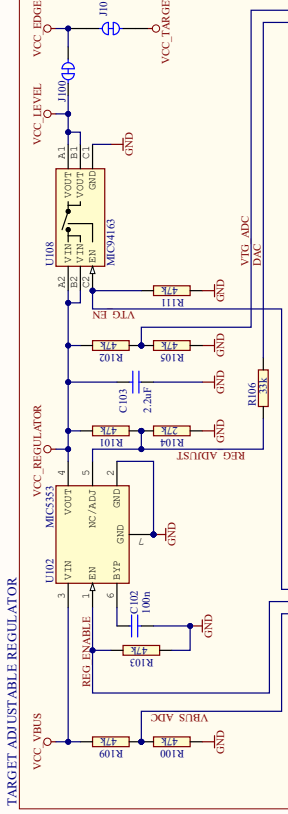


DBG3_CTRL



DEBUGGER

Adjustable output and limitations:
- The debugger can adjust the output voltage of the regulator between 1.25V and 5.1V to the target.
- The level shifters have a minimal voltage level of 1.65V and will limit the minimum operating voltage allowed for the target to still allow communication.
- The output switch has a minimal voltage level of 1.20V and will limit the minimum voltage delivered to the target.
- The target must be able to operate at the minimum voltage level of 1.20V to be within the target's operation.
- Firmware feedback loop will adjust the output voltage accuracy to within 0.5%.



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J113
- Pull down is to avoid L2V on reset line powering R110 during power-on sequence. When debugger is running, DBG3_CTRL is floating.

